



**Protocol API**  
**Ethernet POWERLINK Controlled Node**

**V3.5.0**

**Hilscher Gesellschaft für Systemautomation mbH**

**[www.hilscher.com](http://www.hilscher.com)**

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# Table of Contents

<b>1</b>	<b>Introduction.....</b>	<b>4</b>
1.1	Abstract .....	4
1.2	List of revisions .....	4
1.3	Functional overview .....	5
1.4	System requirements .....	5
1.5	Intended audience.....	5
1.6	Specifications .....	6
1.6.1	Technical data .....	6
1.7	Terms, abbreviations and definitions .....	8
1.8	References to documents .....	9
<b>2</b>	<b>Getting started / Configuration .....</b>	<b>10</b>
2.1	Overview .....	10
2.2	Configuration of POWERLINK Controlled Node .....	11
2.2.1	Using the configuration tool SYCON.net .....	11
2.2.2	Using configuration via packets .....	11
2.3	Running modes .....	13
<b>3</b>	<b>Features.....</b>	<b>14</b>
3.1	Task structure .....	14
3.2	State machine (NMT) .....	16
3.2.1	Initialization phase .....	17
3.2.2	Communication phase .....	19
3.3	Object Dictionary .....	22
3.3.1	Definition of the Object Dictionary .....	22
3.3.2	Indexing concept .....	22
3.3.3	General structure of the Object Dictionary.....	22
3.3.4	Definition of objects .....	23
3.3.5	Accessing the Object Dictionary .....	28
3.3.6	Object Dictionary entries (Communication Profile Area).....	28
3.4	Cyclic data communication / PDO .....	31
3.4.1	Configuring a receive PDO .....	31
3.4.2	Configuring transmit PDO.....	33
3.5	Acyclic data communication / SDO .....	35
3.5.1	SDO Server .....	35
3.6	Diagnosis.....	36
3.6.1	Static Error Bit Field .....	36
3.6.2	Status Entry .....	36
3.6.3	Error Entry .....	36
3.7	Commonly used values in packets .....	37
3.7.1	Values for identifying NMT states in packets.....	37
3.8	Multiplexing .....	37
3.9	NodeID assignment.....	38
3.9.1	Setting NodeID by hardware .....	38
3.9.2	Setting NodeID by software .....	38
3.10	Synchronous DPM IO exchange.....	39
3.11	Including the IO data status into the DPM .....	40
3.12	Cross-traffic .....	41
3.12.1	Configuration of the Managing Node for enabling and configuring the cross traffic feature.....	43
<b>4</b>	<b>Application interface.....</b>	<b>44</b>
4.1	Configuring the Ethernet POWERLINK Controlled Node .....	44
4.1.1	Parameters used in all configuration variants.....	45
4.1.2	Common services (All packet configuration variants) .....	49
4.1.3	Common configuration sequence .....	56
4.1.4	Static mapping configuration with default PDO objects .....	59
4.1.5	Static mapping configuration with user defined PDO objects .....	62
4.1.6	Dynamic mapping configuration .....	64
4.1.7	Optional Services .....	70
4.2	NMT State control .....	73
4.2.1	Architecture of NMT State control .....	73

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4.2.2	Services.....	74
4.2.3	Set State sequence .....	76
4.3	Status indications .....	78
4.3.1	Registration and deregistration of Status Indications .....	78
4.3.2	Common indications.....	82
4.3.3	Indications related to cross traffic .....	86
4.4	Diagnosis.....	89
4.4.1	Error detection .....	89
4.4.2	Error signaling mechanism .....	90
4.4.3	Write Diagnosis to Stack .....	93
4.4.4	Diagnosis indications.....	102
4.4.5	Extended status block .....	107
5	<b>Status codes / Error codes.....</b>	<b>108</b>
6	<b>Appendix .....</b>	<b>109</b>
6.1	LED Status .....	109
6.2	Device description file (XDD) .....	111
6.2.1	Device identification information .....	111
6.2.2	Define objects.....	113
6.2.3	Object Dictionary default values .....	114
6.2.4	PDO configuration .....	115
6.3	Legal notes.....	116
6.4	Third party software licenses .....	119
6.5	List of figures .....	120
6.6	List of tables.....	121
6.7	Contacts .....	123

# 1 Introduction

## 1.1 Abstract

This manual describes the application interface of the Ethernet POWERLINK Controlled Node stack, with the aim to support and lead you during the integration process of the given stack into your own application.

## 1.2 List of revisions

Rev	Date	Name	Revisions
6	2018-03-08	MA,RG	Firmware version V3.4.0 Section <i>Object Dictionary entries (Communication Profile Area)</i> : Index 1F81 and 1F9B added. Section <i>Feature flags</i> : Flags D16 and D9 added. Section <i>Multiplexing</i> added.
7	2018-06-05	HH, MA	Subsections of <i>Using configuration via packets</i> : Bit 1 corrected to bit 2.
			Section <i>Common configuration sequence</i> : Description expanded.
			Section <i>Running modes</i> added.
			Section <i>Extended status block</i> added.
8	2018-09-05	MA, HH	Section <i>NodeID assignment</i> added.
			Section <i>Feature flags</i> : Flag EPL_FEATURE_FLAGS_NODEID_SETUP_BY_SOFTWARE added.
9	2020-10-15	MA,RG,HH	Firmware version V3.5.0 Section <i>Task structure</i> : Functional diagram exchanged Synchronous DPM IO exchange: Data handling changed for use of Hilscher generic service HIL_SET_TRIGGER_TYPE_REQ Added section on <i>Including the IO data status into the DPM</i> Added section on <i>Cross-traffic</i> . Also added subsection <i>Indications related to cross traffic</i> Sections <i>Stack Configuration Flags</i> and <i>Dynamic mapping configuration</i> have been adapted.

Table 1: List of revisions

## 1.3 Functional overview

This stack has been written to meet the requirements outlined in the Ethernet POWERLINK (EPL) specification. The user of this stack is provided with a fully functional general-purpose Software package with the following main features:

- Implementation of the EPL- state machine
- Implementation of the CANopen-style object dictionary according to the EPL specification

## 1.4 System requirements

This software package has the following environmental system requirements:

- netX-Chip as CPU hardware platform
- Operating system for task scheduling required

## 1.5 Intended audience

This manual is suitable for software developers with the following background:

- Knowledge of the programming language C
- Knowledge of the use of the real time operating system rcX
- Knowledge of the Ethernet Powerlink V 2.0 Specification

## 1.6 Specifications

The data below applies to the POWERLINK Controlled Node firmware and stack version V3.5.0.

### 1.6.1 Technical data

#### State machine

Implementation of the EPL-state machine

#### Object dictionary

Implementation of the CANopen-style object dictionary according to the EPL specification

#### Technical data

Maximum number of cyclic input data	1490 bytes (800 bytes on netX52)
Maximum number of cyclic output data	1490 bytes (800 bytes on netX52)
Acyclic data transfer	SDO Upload/Download
Functions	SDO over ASnd and UDP Slave-to-slave communication
Baud rate	100 MBit/s, half-duplex
Data transport layer	Ethernet II, IEEE 802.3
Ethernet Powerlink version	V 2

Table 2: Technical data

#### Firmware/stack available for netX

netX50	no
netX 51, netX52	yes
netX 100, netX 500	yes

Table 3: Availability of firmware/stack

#### Configuration

Configuration by packet to transfer warm-start parameters

#### Diagnostic

Firmware supports common diagnostic in the dual-port-memory for loadable firmware

**Limitations**

Stack supports and handles only one network interface

Slave-to-slave communication: Max. 8 communicating slaves supported (Loadable firmware)

Max. 254 communicating slaves supported (LOM)

## 1.7 Terms, abbreviations and definitions

Term	Description
AP	Application
API	Application Programming Interface
ARP	Address Resolution Protocol
ASnd	Asynchronous Send
CN	Controlled Node
CRC	Cyclic Redundancy Check
DLL	Data Link Layer
DPM	Dual-Port Memory
EPL	Ethernet POWERLINK
EPSSG	Ethernet POWERLINK Standardization Group
HAL	Hardware Abstraction Layer
IEEE	Institute of Electrical and Electronic Engineers
IF	Interface
IP	Internet Protocol
LSB	Least significant byte
MN	Managing Node
MSB	Most significant byte
NMT	Network Management
OD	Object Dictionary
ODV3	Object Dictionary Version 3
PDO	Process Data Object
PReq	Poll Request
PRes	Poll Response
RAM	Random Access Memory
RxPDO	Receive PDO
SoA	Start of Asynchronous
SoC	Start of Cyclic/Start of Cycle
SDO	Service Data Object
TCP	Transmission Control Protocol
UDP	User Datagram Protocol
TxPDO	Transmit PDO
XDD	XML Device Description
XML	Extensible Markup Language

Table 4: Terms, abbreviations and definitions

All variables, parameters, and data used in this manual have the LSB/MSB ("Intel") data representation. This corresponds to the convention of the Microsoft C Compiler.



## 1.8 References to documents

This document is based on the following specifications:

1. Hilscher Gesellschaft für Systemautomation mbH: Dual-Port Memory Interface Manual, netX Dual-Port Memory Interface. Revision 17, English, 2020.
2. Hilscher Gesellschaft für Systemautomation mbH: Packet API, netX Dual-Port Memory, Packet-based services (netX 10/50/51/52/100/500), Revision 4, 2020.
3. Ethernet Powerlink Communication Profile Specification; EPSG DS 301 V1.4.0; 2020.
4. Hilscher Gesellschaft für Systemautomation mbH: Protocol API, Object Dictionary. Revision 4, 2017.
5. Ethernet Powerlink XML Device Description; EPSG DS 311 V1.2.0; 2016.

*Table 5: References to documents*

## 2 Getting started / Configuration

This section explains some essential information you should know when starting to work with the Ethernet POWERLINK Controlled Node Protocol API.

### 2.1 Overview

You can find the most commonly used functionality of the Ethernet POWERLINK Controlled Node Protocol API within the following sections of this document:

Section number	Section	Page
4.1.2.1	Configure Stack service	49
3.4	Cyclic data communication / PDO	31
3.5	Acyclic data communication / SDO	35
3.3	Object Dictionary	22
4.2	NMT State control	73
4.4	Diagnosis	89

Table 6: Overview

## 2.2 Configuration of POWERLINK Controlled Node

The POWERLINK Controlled Node can be configured by using different means. This includes the following methods:

- Configuration via SYCON.net
- Configuration via packets
  - Static mapping configuration with default PDO objects created by the Controlled Node stack.
  - Static mapping configuration with PDO objects defined and created by the application.
  - Dynamic mapping configuration with PDO objects defined and created by the application.

### 2.2.1 Using the configuration tool SYCON.net

This configuration method is described in the tool documentation.

### 2.2.2 Using configuration via packets

The following sections will outline the different variants for configuring the Controlled Node via packets.

These parameters of *Configure Stack service* (page 49) are evaluated for all variants:

- Automatic or application triggered start of Controlled Node
- Automatic or application triggered running mode of Controlled Node state machine
- PReq exchange triggered from bus or from application
- Enable or disable PDO mapping version check
- Enable signaling transmit data validity using the DPM ApplicationReady flag
- Identity parameters (vendor and product identification data)
- POWERLINK communication parameters:
  - Cycle length
  - Node address
  - Error thresholds
  - Status entries
  - PDO size

### 2.2.2.1 Static mapping configuration with default PDO objects created by the Controlled Node stack

This configuration method is selected by keeping cleared the bit 2 in the field `ulStackCfgFlags` and the bit 6 in the field `ulFeatureFlags` of the *Configure Stack service* on page 49. For details, see chapter *Static mapping configuration with default PDO objects* on page 59.

When using this configuration way, the Controlled Node will configure default PDO objects based on the given data size. The stack will be the own responsible of these objects and their handling.

The mapping between the cyclic data stream on the bus and the created PDO objects is static.

In the cyclic communication, the application will just have to handle with transmit and receive data streams.

Acyclic access to the PDO objects will be handled in the Controlled Node stack.

### 2.2.2.2 Static mapping configuration with PDO objects defined and created by the application

This configuration method is selected by setting the bit 2 in the field `ulStackCfgFlags` and keeping the bit 6 cleared in the field `ulFeatureFlags` of the *Configure Stack service* on page 49. For details, see chapter *Static mapping configuration with user defined PDO objects* on page 62.

When using this configuration way, the application is responsible to create and handle the access to the PDO objects.

The application has also to define and implement the mapping configuration between data stream in the bus and its own PDO objects. This mapping configuration is static.

In the cyclic communication, the application will exchange with the DPM the same data stream as in the bus and it is up to the application to map the data to the corresponding PDO objects depending on the configuration.

Acyclic access to the PDO objects has to be handled in the application.

Use the Object Dictionary V3 (ODV3) packets to create and handle the PDO objects.

---

**Note:** It is mandatory to create the necessary PDO objects and implement all corresponding access handlings.

---

### 2.2.2.3 Dynamic mapping configuration with PDO objects defined and created by the application

This configuration method is selected by setting the bit 2 in the field `ulStackCfgFlags` and the bit 6 in the field `ulFeatureFlags` of *Configure Stack* service on page 49. For details, see chapter *Dynamic mapping configuration* on page 64.

When using this configuration way, the application is responsible to create and handle the access to the PDO objects.

The application has also to define and implement a default mapping configuration between data stream in the bus and its own PDO objects. This mapping is dynamic and may be changed by the bus.

In the cyclic communication, the application will exchange with the DPM the same data stream as in the bus and it is up to the application to map the data to the corresponding PDO objects depending on the configuration.

Acyclic access to the PDO objects has to be handled in the application.

Use the Object Dictionary V3 (ODV3) packets to create and handle the PDO objects. These are described in reference [4].

---

**Note:** It is mandatory to create the necessary PDO objects and implement all corresponding access handlings.

---

## 2.3 Running modes

The Controlled Node stack provides two running modes if configured via packets.

- Automatic running mode: After the Controlled Node is configured and started; the complete state machine is triggered automatically.
- Application triggered mode: In this mode the application is responsible to trigger the state machine in the initialization phase and to confirm the `EnableReadyToOperate` command received from the bus.

## 3 Features

### 3.1 Task structure

The figure below displays the internal structure of the tasks which together represent the Ethernet POWERLINK Controlled Node stack:

**Powerlink Stack V3.5**

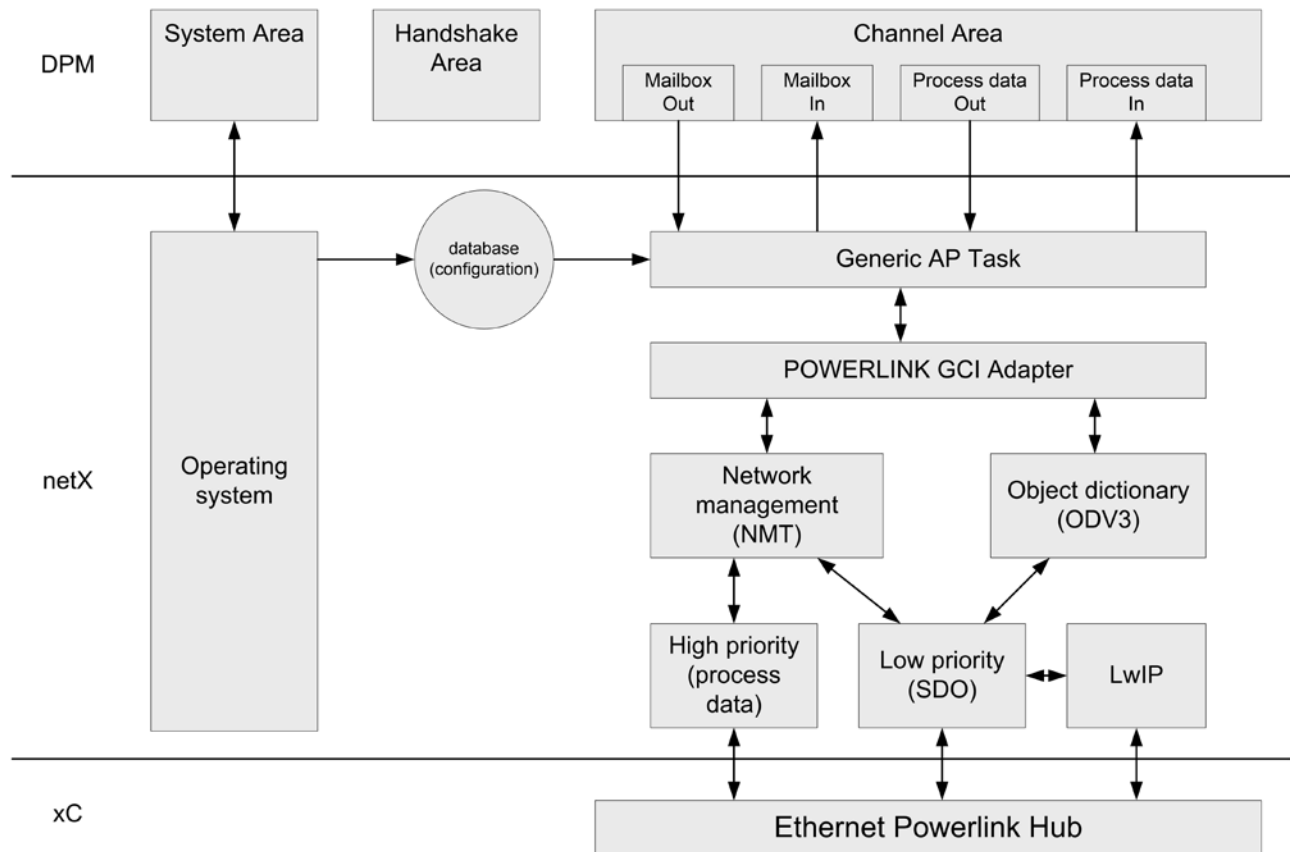


Figure 1: Structure of Ethernet POWERLINK Controlled Node stack

The dual-port memory is used to exchange the information such as status, configuration messages, service and process data between netX EPL firmware and host application.

The user application only accesses the task located in the highest layer namely the AP task which constitutes the application interface of the Ethernet POWERLINK Controlled Node stack.

The POWERLINK Controlled Node Stack consists of the following components running on netX core:

- Generic AP task (DPM handling)
- Powerlink GCI Adapter task (Interface to stack)
- Network management (NMT-Task)
- ODV3 (Object Dictionary access)
- SDO-Task (SDO server)
- ASnd layer (part of SDO-Task)
- LwIP (TCP/IP stack)

- DrvEth (Interface between stack and HAL. Includes the DLL state machine)

### **Generic AP task**

This task is actually present in all stack implementations for netX controllers (using DPM) and has basically the same functionality in all variants. This task handles the interface to DPM, routes the received packets to the IF task, handles the indicators (LED) in accordance with the indications received from the stack and updates the IO data.

### **Powerlink GCI Adapter task**

This task is responsible for the translation between packets and function interfaces. This task makes the first validations of the received packets before calling the stack API functions or if necessary routes them again to other components of the stack, e.g. ODV3.

### **Network management (NMT task)**

This is the main task of the stack within the high level state machine (NMT state machine). It is responsible for the following:

- Configuration of stack
  - Setting default and user values to the different stack parameters
  - Creation of the default communication OD
- Handling of write and read accesses to the created default OD
- NMT state machine
- Error handling
  - Communication errors e.g. loss of frames, are detected by the DLL state machine and signaled here. The NMT component is responsible for the corresponding handling
  - Vendor or profile specific errors are detected by the application and have also to be signaled to this task.
- Status entries handling
  - If the application wants to send a status entry over the bus, this has to be set in the NMT component. The task will handle the inclusion of this information in the EPL communication
- Handling of the received NMT command from the bus.

### **ODV3**

This is a generic component which is responsible for the management of the Object Dictionary (OD). It provides functionalities to create and handle objects within their index and subindex structures.

### **SDO task**

This task handles the access to the local OD from the bus (SDO server) and provides functionalities in order to access the OD of other EPL nodes (SDO client).

### ASnd layer

This is an abstraction layer for the ASnd frames. POWERLINK protocol supports asynchronous frames via normal EPL as well as via UDP. The requested service within these frames may be handled in different components e.g. NMT commands have to be handled in the NMT task.

The ASnd abstraction layer provides register functionality for specific services as well as functionalities to send own ASnd frames. The ASnd abstraction layer receives the asynchronous frames (both EPL and UDP) and sends the content to the corresponding component.

---

**Note:** The application can only register for the manufacturer specific services (range 0xA0.0xFE). Other services are internally handled or not supported.

---

### LwIP

This component receives all IP and ARP frames from the bus and handles them. It provides common IP-based communication functionalities e.g. listening to a specific UDP port or sending of own IP frames.

---

**Note:** The local UDP port 3819 is used for POWERLINK specific UDP/IP frames.

---

### DrvEth for Powerlink

This component is the lower level of the POWERLINK Controlled Node stack. It is the interface between the high level stack (chip independent) and the HAL (chip specific).

This task is responsible for the following:

- Configuration of the HAL.
- Receiving and handling of frames (IRQ). Route the frames to the correspondent components e. g. PReq frames are sent directly to the AP-Task.
- Handling of the lower level stack state machine (DLL). This state machine handles the communication in the POWERLINK cycle. It tracks the order of the frames received in a cycle and detects errors in the communication. These errors are signaled to the high level of the stack (NMT) for the corresponding handling. Since this state machine has no direct effect on the application, it is not explained in more details in this manual. For more information please refer to reference 3 (Ethernet Powerlink Communication Profile Specification; EPSG DS 301 V1.4.0; 20, chapter 4.2.4.5 "CN Cycle State Machine").

## 3.2 State machine (NMT)

This section explains the general states of the POWERLINK Controlled Node. The current state of the stack is part of the information contained in the EPL frames. The application is also informed about changes of state by the corresponding indications (see section *Current NMT State indication* on page 82)

The NMT state machine can be split in two super states:



- Initialization phase (NMT\_GS\_INITIALISATION).
- Communicating phase (NMT\_GS\_COMMUNICATING).

An overview of the stack states is provided in the next subsections. For more information about the NMT state machine please refer to reference 3, chapter 7 “Network Management (NMT)”.

### 3.2.1 Initialization phase

This is the initialization phase of the stack and corresponds to the super state NMT\_GS\_INITIALISATION. In this super state, the node is not communicating with the bus yet.

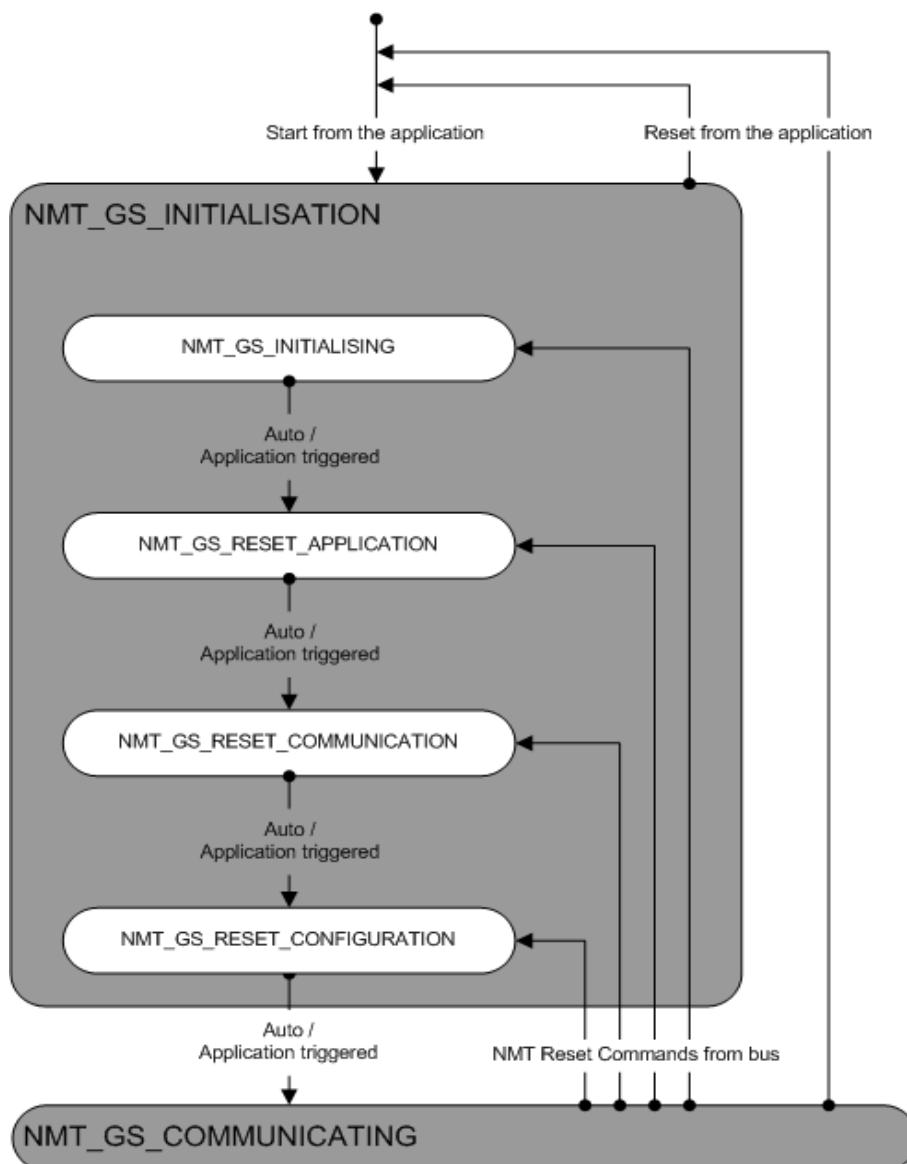


Figure 2: State diagram of the Ethernet POWERLINK Controlled Node - Initialization

The stack enters this phase after receiving the BusOn request from the application. If a reset command is received from the bus while in communicating phase, the stack automatically changes its state to the requested state in the initialization phase.

The stack leaves this phase to change to communicating when the initialization is complete or to start again if the application requests a reset.

The transition between the states of the initialization super state occurs automatically or triggered by application depending on the received configuration (see section *Stack Configuration Flags* on page 45).

In the initialization phase, the POWERLINK Controlled Node stack may be in one of the following states:

- NMT\_GS\_INITIALISING
- NMT\_GS\_RESET\_APPLICATION
- NMT\_GS\_RESET\_COMMUNICATION
- NMT\_GS\_RESET\_CONFIGURATION

### **3.2.1.1 NMT\_GS\_INITIALISING**

In this state the stack parameters are initialized with their default or user specific values (from the configuration).

### **3.2.1.2 NMT\_GS\_RESET\_APPLICATION**

In this state the parameters of the manufacturer-specific profile and of the standardized device profile are set to their Power On values (Set values in OD).

### **3.2.1.3 NMT\_GS\_RESET\_COMMUNICATION**

In this state the parameters of the communication profile are set to their Power On values (Set values in OD).

### **3.2.1.4 NMT\_GS\_RESET\_CONFIGURATION**

Until this point the whole configuration was stored in the OD. In the last state of the initialization phase, the active node configuration is generated based on the parameter set stored within the object dictionary.

Now the stack is configured and the communication over the bus can begin.

### 3.2.2 Communication phase

This corresponds to the super state (NMT\_GS\_COMMUNICATING). When entering this super state, the node starts the communication over the bus. At this point, the handling of received frames begins.

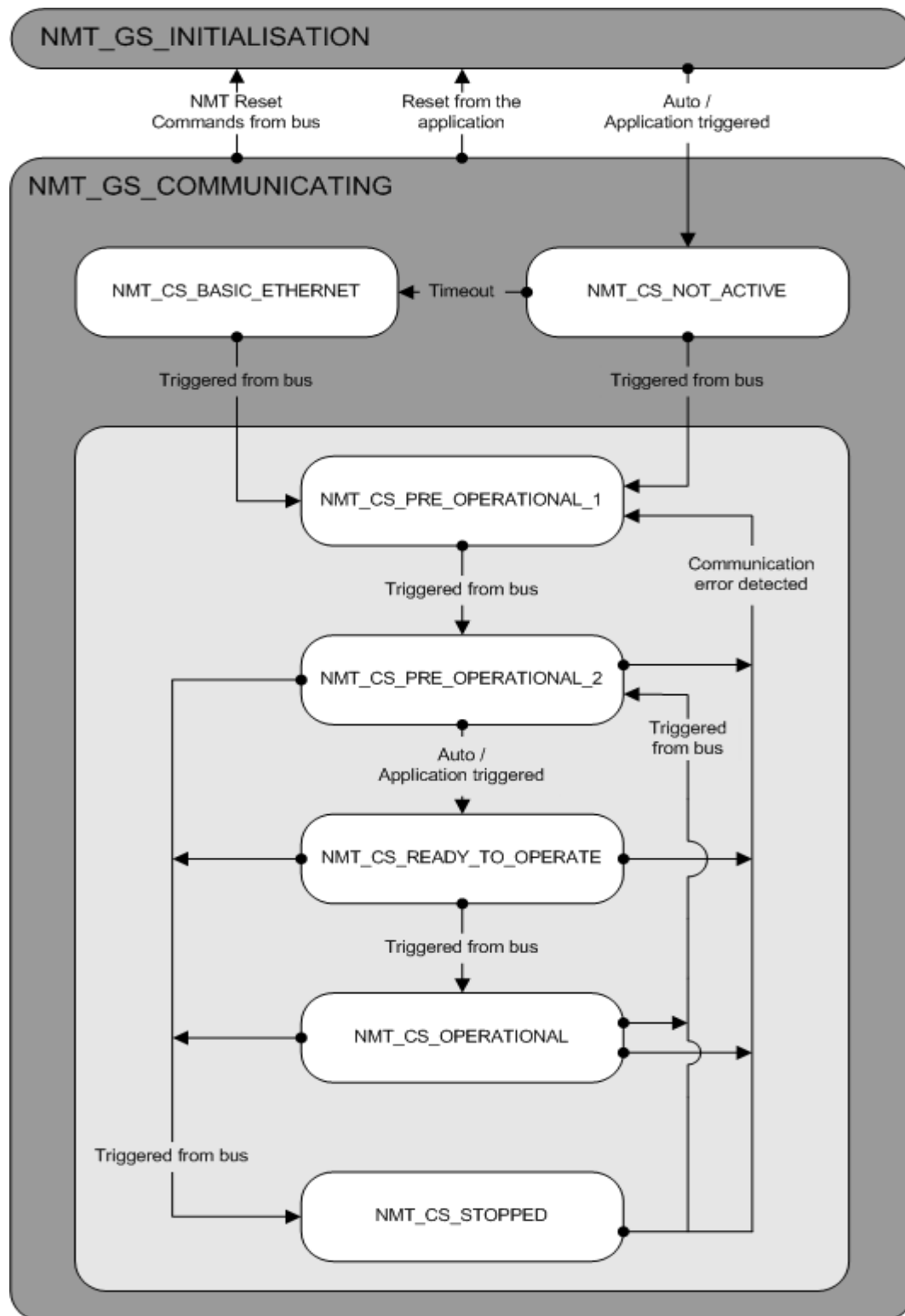


Figure 3: State diagram of the Ethernet POWERLINK Controlled Node - Communicating

The stack enters this phase after the initialization phase is completed.

The stack leaves this phase when a reset command from the bus or the application is received.

In the communicating phase, the POWERLINK Controlled Node stack may be in one of the following states:

- NMT\_CS\_NOT\_ACTIVE
- NMT\_CS\_BASIC\_ETHERNET
- NMT\_CS\_PRE\_OPERATIONAL\_1
- NMT\_CS\_PRE\_OPERATIONAL\_2
- NMT\_CS\_READY\_TO\_OPERATE
- NMT\_CS\_OPERATIONAL
- NMT\_CS\_STOPPED

### 3.2.2.1 NMT\_CS\_NOT\_ACTIVE

This is the first state after the initialization phase. The node begins tracking the bus and analyzing the received frames. If an EPL frame has been received the stack switches to NMT\_CS\_PRE\_OPERATIONAL\_1. If no EPL frames have been received within a defined timeout (Basic Ethernet Timeout), the node switches automatically to NMT\_CS\_BASIC\_ETHERNET.

### 3.2.2.2 NMT\_CS\_BASIC\_ETHERNET

This state is entered, if after connecting to the bus, the node does not receive any EPL frames within a defined timeout.

In this state, the node works according to IEEE 802.3 and may perform Legacy Ethernet communication. No Ethernet POWERLINK cycle is available in this state.

The node changes its state to NMT\_CS\_PRE\_OPERATIONAL\_1 if a valid EPL frame is received.

---

**Note:** Only in this state the node is able to send frames autonomously. In all other states, the node needs permissions from the MN to send frames.

---

### 3.2.2.3 NMT\_CS\_PRE\_OPERATIONAL\_1

This is the first state within valid EPL network communication. Here the MN starts configuring the node. The node proceeds to the next state after detecting a SoC frame on the bus (Start of cycle).

### **3.2.2.4 NMT\_CS\_PRE\_OPERATIONAL\_2**

This state is entered, if an Ethernet POWERLINK cycle is detected (SoC frames are received cyclically). In this state the MN continues configuration of the node.

From this moment, the node is part of the EPL cycle and if a communication error is detected e.g. loss of frames, the node returns to NMT\_CS\_PRE\_OPERATIONAL\_1. Also from this point, if the command NMT\_StopNode is received from the bus, the node immediately goes to NMT\_CS\_STOPPED.

After the complete configuration has been received from the bus, the MN sends the command NMT\_EnableReadyToOperate. The node has to confirm this by changing to the next state. This happens either automatically or triggered by the application depending on the configuration parameters (see section Stack Configuration Flags on page 45).

### **3.2.2.5 NMT\_CS\_READY\_TO\_OPERATE**

This state is used to confirm the command NMT\_EnableReadyToOperate to the MN. This signals that the node is configured and waiting for order to go to NMT\_CS\_OPERATIONAL.

This change happens when receiving the command NMT\_StartNode.

### **3.2.2.6 NMT\_CS\_OPERATIONAL**

In this state the node is configured and the cyclic IO data exchange starts.

### **3.2.2.7 NMT\_CS\_STOPPED**

This state is entered if the command NMT\_StopNode is received while in valid EPL cycle. In this state the node continues to be part of a valid EPL cycle, but no valid IO data exchange is performed.

The command NMT\_EnterPreoperational2 triggers the return to the state NMT\_CS\_PRE\_OPERATIONAL\_2.

## 3.3 Object Dictionary

This section provides an overview of the Object Dictionary concept and handling. For a detailed description refer to reference 4 Hilscher Gesellschaft für Systemautomation mbH: Protocol API, Object Dictionary.

### 3.3.1 Definition of the Object Dictionary

The object dictionary is a special area for the storage of parameters, application data and the mapping information between process data and application data (PDO mapping). In order to use CANopen-based device and application profiles in Ethernet POWERLINK Controlled Node, the object dictionary functionality is similar to the one defined in the CANopen standard. Access to the object dictionary is possible via Service Data Objects (SDO) which provide mailbox-based access functionality.

- All CANopen-related data objects are contained in the object dictionary and can be accessed in a standardized manner. You can view the object dictionary as a container for device parameter data structures.

### 3.3.2 Indexing concept

Indexing is generally done via two values, namely the index and the sub-index. The index governs which function will be performed generally. Indices are ordered in ranges of functionality effective within the same area, see below. The sub-index, however, determines which detailed part of an array, record or structure will be active, i.e. which function will be performed.

### 3.3.3 General structure of the Object Dictionary

The object dictionary is structured in separate areas. Each area has its own range of permitted index values and its special purpose as defined in the table below:

Index range	Area name	Purpose
0x0000 – 0x0FFF	Data Type Area	Definition and description of data types.
0x1000 – 0x1FFF	Communication Profile Area	Definition of generally applicable variables (communication objects for all devices as defined by CANopen standard DS 301).
0x2000 – 0x5FFF	Manufacturer-specific Area	Definition of manufacturer-specific variables
0x6000 – 0x9FFF	Profile Area	Definition of variables related to a specific profile
0xA000 – 0xFFFF	Reserved Area	This area is reserved for future use

Table 7: General structure of Object Dictionary

### 3.3.4 Definition of objects

The object dictionary contains descriptions of objects. Each entry of the object dictionary represents the description of one single object. It contains the following object-related information:

- The index of the object
- The object code (classification of type, see explanation below)
- The name of the object
- The data type of the object
- The attributes of the object
- The information whether the object is mandatory or optional.

#### Index

The index is used for addressing and referencing purposes. It contains the position of the entry within the object dictionary. Complex objects may also be addressed by index and sub-index.

#### Object code

The following object codes providing different classes of objects may be defined within the object dictionary:

Object code	Object name
0002	DOMAIN
0005	DEFTYPE
0006	DEFSTRUCT
0007	VAR
0008	ARRAY
0009	RECORD

Table 8: Definition of Objects

- A domain can be seen as a large amount of data regardless of its structure, for instance a piece of executable code.
- DEFTYPE contains the definition of a simple data type such as Boolean, unsigned16 or float.
- DEFSTRUCT contains the type definition of a structured data object (i.e. it is composed of parts) such as for instance a record.
- VAR contains a value of a simple data type.
- ARRAY contains a multiple data field of values of the same type.
- RECORD contains a multiple data field of values of different types.

**Name**

The name component of the entry should give a clear and short textual description of the purpose or function of the object.



### Access rights

The attribute indicates access rights (as bit flags) such as read or write access is allowed or prohibited.

Bit flag	Access right if set
0x0001	Read in NMT_CS_PRE_OPERATIONAL_1
0x0002	Read in NMT_CS_PRE_OPERATIONAL_2
0x0004	Read in NMT_CS_READY_TO_OPERATE
0x0008	Read in NMT_CS_OPERATIONAL
0x0010	Read in NMT_CS_STOPPED
0x0020	Read in NMT_CS_BASIC_ETHERNET
0x0040	Read in NMT_CS_NOT_ACTIVE
0x0080	Read during reset of the node
0x00FF	Read in all states
0x0100	Write in NMT_CS_PRE_OPERATIONAL_1
0x0200	Write in NMT_CS_PRE_OPERATIONAL_2
0x0400	Write in NMT_CS_READY_TO_OPERATE
0x0800	Write in NMT_CS_OPERATIONAL
0x1000	Write in NMT_CS_STOPPED
0x2000	Write in NMT_CS_BASIC_ETHERNET
0x4000	Write in NMT_CS_NOT_ACTIVE
0x8000	Write during reset of the node
0xFF00	Write in all states

Table 13: Available access rights for POWERLINK objects

## Data types

The following data types are available:

Data type index	Name	Object
0001	BOOLEAN	DEFTYPE
0002	INTEGER8	DEFTYPE
0003	INTEGER16	DEFTYPE
0004	INTEGER32	DEFTYPE
0005	UNSIGNED8	DEFTYPE
0006	UNSIGNED16	DEFTYPE
0007	UNSIGNED32	DEFTYPE
0008	REAL32	DEFTYPE
0009	VISIBLE_STRING	DEFTYPE
000A	OCTET_STRING	DEFTYPE
000B	UNICODE_STRING	DEFTYPE
000C	TIME_OF_DAY	DEFTYPE
000D	TIME_DIFFERENCE	DEFTYPE
000E	Reserved	
000F	DOMAIN	DEFTYPE
0010	INTEGER24	DEFTYPE
0011	REAL64	DEFTYPE
0012	INTEGER40	DEFTYPE
0013	INTEGER48	DEFTYPE
0014	INTEGER56	DEFTYPE
0015	INTEGER64	DEFTYPE
0016	UNSIGNED24	DEFTYPE
0017	Reserved	
0018	UNSIGNED40	DEFTYPE
0019	UNSIGNED48	DEFTYPE
001A	UNSIGNED56	DEFTYPE
001B	UNSIGNED64	DEFTYPE
001C-0022	Reserved for future use	
0023	IDENTITY	DEFSTRUCT
0024-003F	Reserved	
0040-005F	Manufacturer Specific Complex Data Types	DEFSTRUCT
0060-007F	Device Profile 0 Specific Standard Data Types	DEFTYPE
0080-009F	Device Profile 0 Specific Complex Data Types	DEFSTRUCT
00A0-00BF	Device Profile 1 Specific Standard Data Types	DEFTYPE
00C0-00DF	Device Profile 1 Specific Complex Data Types	DEFSTRUCT

00E0-00FF	Device Profile 2 Specific Standard Data Types	DEFTYPE
0100-011F	Device Profile 2 Specific Complex Data Types	DEFSTRUCT
0120-013F	Device Profile 3 Specific Standard Data Types	DEFTYPE
0140-015F	Device Profile 3 Specific Complex Data Types	DEFSTRUCT
0160-017F	Device Profile 4 Specific Standard Data Types	DEFTYPE
0180-019F	Device Profile 4 Specific Complex Data Types	DEFSTRUCT
01A0-01BF	Device Profile 5 Specific Standard Data Types	DEFTYPE
01C0-01DF	Device Profile 5 Specific Complex Data Types	DEFSTRUCT
01E0-01FF	Device Profile 6 Specific Standard Data Types	DEFTYPE
0100-021F	Device Profile 6 Specific Complex Data Types	DEFSTRUCT
0220-023F	Device Profile 7 Specific Standard Data Types	DEFTYPE
0240-025F	Device Profile 7 Specific Complex Data Types	DEFSTRUCT
0260-0400	Reserved	Reserved
0401	MAC_ADDRESS	DEFTYPE
0402	IP_ADDRESS	DEFTYPE
0403-041F	Reserved	Reserved
0420	PDO_CommParam_Record_TYPE	DEFSTRUCT
0422	SDO_ParameterRecord_TYPE	DEFSTRUCT
0423	Reserved	Reserved
0424	DLL_ErrorCntRec_TYPE	DEFSTRUCT
0425	NWL_IpGroup_TYPE	DEFSTRUCT
0426	NWL_IpAddrTable_TYPE	DEFSTRUCT
0427-0428	Reserved	Reserved
0429	NMT_ParameterStorage_TYPE	DEFSTRUCT
042A	Reserved	Reserved
042B	NMT_InterfaceGroup_Xh_TYPE	DEFSTRUCT
042C	NMT_CycleTiming_TYPE	DEFSTRUCT
042E-0434	Reserved	Reserved
0435	CFM_VerifyConfiguration_TYPE	DEFSTRUCT
0436-0438	Reserved	Reserved
0439	NMT_EPLNodeID_TYPE	DEFSTRUCT
043A-0FFF	Reserved	Reserved

Table 9: Available Data Type Definitions – Part 2

Further description details of these data types can be found in the Ethernet Powerlink specification (reference 3, chapter 6.1.)

### 3.3.5 Accessing the Object Dictionary

The Object Dictionary module (ODV3) provides its own packet API set. This is described in reference [4].

### 3.3.6 Object Dictionary entries (Communication Profile Area)

According to the Ethernet Powerlink standard, the Communication Profile Area located at the index range from 0x1000 to 0x1FFFh contains the communication specific parameters for the entire network. These entries are common for all devices.

The following table provides an overview of the relevant objects of the Communication Profile Area for a Powerlink Controlled Node, which can be supported by the Hilscher's stack. The table also shows whenever an object is directly allocated and handled by the stack. Some of those objects e.g. the identity object get their content from the configuration set sent by the application:

Object Dictionary entries for Communication Profile					
Data type index	Object	Name	Type	M/O/C	Created and handled by stack
1000	VAR	Device type	UNSIGNED32	M	Yes
1001	VAR	Error register	UNSIGNED8	M	Yes
1003	ARRAY	Error history	DOMAIN	O	Yes
1006	VAR	Communication cycle period	UNSIGNED32	M	Yes
1008	VAR	Manufacturer Device Name	VISIBLE_STRING	O	No
1009	VAR	Manufacturer Hardware Version	VISIBLE_STRING	O	No
100A	VAR	Manufacturer Software Version	VISIBLE_STRING	O	No
1010	RECORD	Store parameters	DEFSTRUCT	O	No
1011	RECORD	Restore default parameters	DEFSTRUCT	O	No
1016	ARRAY	Consumer heartbeat time	UNSIGNED32	O	No
1018	RECORD	Identity object	DEFSTRUCT	M	Yes
1020	RECORD	Verify configuration	DEFSTRUCT	M	Yes
1021	VAR	Store device description file	DOMAIN	O	No
1022	VAR	Store device description format	UNSIGNED16	C <sup>1</sup>	No
...	...	...	...	...	...
1030	RECORD	Interface group 0	DEFSTRUCT	M	Yes
1031	RECORD	Interface group 1	DEFSTRUCT	O <sup>2</sup>	No
...	...	...	...	...	...

<sup>1</sup> Object 0x1022 is mandatory to be created if the object 0x1021 is also created.

<sup>2</sup> Object 0x1031...0x1039 should not be implemented since the Hilscher EPL CN stack supports and handles only one network interface.

Object Dictionary entries for Communication Profile					
Data type index	Object	Name	Type	M/O/C	Created and handled by stack
1039	RECORD	Interface group 9	DEFSTRUCT	O	No
1101	RECORD	NMT telegrams counter	DEFSTRUCT	O	No
1102	RECORD	Error statistics	DEFSTRUCT	O	No
....	....	....	....	....	....
1200	RECORD	SDO Server container parameter 0	DEFSTRUCT	O	No
....	....	....		....	....
127F	RECORD	SDO Server container parameter 127	DEFSTRUCT	O	No
1280	RECORD	SDO Client container parameter 0	DEFSTRUCT	O	No
....	....	....		....	....
12FF	RECORD	SDO Client container parameter 127	DEFSTRUCT	O	No
1300	VAR	SDO sequence layer timeout	UNSIGNED32	M	Yes
1301	VAR	SDO command layer timeout	UNSIGNED32	O	No
1302	VAR	SDO sequence layer number of Ack	UNSIGNED32	O	No
1400	RECORD	PDO Rx communication parameters 0	DEFSTRUCT	C <sup>1</sup>	Yes <sup>2</sup>
1600	ARRAY	PDO Rx mapping parameters 0	UNSIGNED64	C <sup>1</sup>	Yes <sup>2</sup>
1800	RECORD	PDO Tx communication parameters 0	DEFSTRUCT	C <sup>3</sup>	Yes <sup>2</sup>
1A00	ARRAY	PDO Tx mapping parameters 0	UNSIGNED64	C <sup>3</sup>	Yes <sup>2</sup>
1C0A	RECORD	DLL CN collision	DEFSTRUCT	O	Yes
1C0B	RECORD	DLL CN loss SoC	DEFSTRUCT	M	Yes
1C0C	RECORD	DLL CN loss SoA	DEFSTRUCT	O	Yes
1C0D	RECORD	DLL CN loss PReq	DEFSTRUCT	O	Yes
1C0E	RECORD	DLL CN SoC Jitter	DEFSTRUCT	O	Yes
1C0F	RECORD	DLL CN CRC error	DEFSTRUCT	O	Yes
1C13	VAR	DLL Cn SoC Jitter Range	UNSIGNED32	O	Yes
1C14	VAR	DLL Cn loss of SoC tolerance	UNSIGNED32	M	Yes
1E40	RECORD	IP address table 0	DEFSTRUCT	C	Yes
1E41	RECORD	IP address table 1	DEFSTRUCT	C <sup>4</sup>	No

<sup>1</sup> Objects 0x1400 and 0x1600 are mandatory if the node supports Rx data

<sup>2</sup> Objects 0x1400, 0x1600, 0x1800 and 0x1A00 may be created by the stack, depending on the configuration received from the application

<sup>3</sup> Objects 0x1800 and 0x1A00 are mandatory if the node supports Tx data

<sup>4</sup> Object 0x1E41...0x1E49 should not be implemented since the Hilscher EPL CN stack supports and handles only one network interface.

Object Dictionary entries for Communication Profile					
Data type index	Object	Name	Type	M/O/C	Created and handled by stack
----	----	----	----	----	----
1E49	RECORD	IP address table 9	DEFSTRUCT	C	No
1E4A	RECORD	IP group	DEFSTRUCT	C	Yes
1F81	ARRAY	Node Assignment	UNSIGNED32	C	Yes
1F82	VAR	Feature flags	UNSIGNED32	M	Yes
1F83	VAR	EPL version	UNSIGNED8	M	Yes
1F8C	VAR	Current NMT state	UNSIGNED8	M	Yes
1F93	RECORD	EPL Node Id	DEFSTRUCT	M/C <sup>1</sup>	Yes
1F98	RECORD	Cycle timing	DEFSTRUCT	M	Yes
1F99	VAR	Basic Ethernet timeout	UNSIGNED32	M	Yes
1F9A	VAR	Host name	VISIBLE_STRING	C	Yes
1F9B	ARRAY	Multiple Cycle Assignment	UNSIGNED8	C	Yes
1F9E	VAR	Reset command	UNSIGNED8	M	Yes

Table 10: Communication Profile - General Overview

For more detailed information about the complete communication, there are profile objects and their structures, please refer to Ethernet Powerlink Communication Profile Specification; EPSG DS 301 V1.4.0; 20 (App. 1), reference [3].

<sup>1</sup> Subindex 3 of object 0x1F93 is only created and handled if the node supports NodeIdBySw (not supported yet)

## 3.4 Cyclic data communication / PDO

Within all CANopen-based communication systems (such as Ethernet Powerlink), cyclic communication is done by PDOs (Process Data Objects). These PDOs are used to transfer time-critical process data in real-time. PDO data are exchanged exclusively in the cyclic time slot of the Ethernet Powerlink data transmission cycle.

A PDO defines a storage area for data which is cyclically filled up with current data again. These data can be assigned to subobjects of objects stored in the Ethernet Powerlink object dictionary (i.e. addressing by index and subindex is applied). The process of assignment of cyclic data from the PDO to various objects within the object dictionary is denominated as PDO mapping.

In order to practically define a PDO mapping within the object dictionary, you need to know two objects, namely

- The PDO Communication Parameter Object.
- The PDO Mapping Object

Such objects are defined separately for receive and transmit PDO's, see below.

---

**Note:** You can assign 240 receive PDOs (RxPDO) to an Ethernet POWERLINK Controlled Node, but only one transmit PDO (TxPDO).

---

### 3.4.1 Configuring a receive PDO

The Ethernet POWERLINK Controlled Node requires the objects 0x1400...0x14FF and 0x1600...0x16FF to configure a receive PDO. These objects contain the following information:

- Source of the data
- Mapping version
- Mapping configuration

#### Source of the data

The EPL node, from which the data are received, has to be configured in the objects 0x1400...0x14FF in subindex 1. Following values are allowed:

- 0: This received data in this PDO corresponds to the PReq sent from the MN (This is the common setting for object 0x1400).
- Own node ID: Own PRes (Transmit data) will be mapped into this receive PDO.
- Other: Pres from other node ID is mapped into this receive PDO.

## Mapping version

This parameter allows checking the validity of the current IO data with the configured mapping.

The mapping version is configured in the object 0x1400...0x14FF in subindex 2.

This parameter is a byte where:

- Bits 4-7 contain the main version number
- Bits 0-3 contain the sub version number

---

**Note:** The mapping version is received into PReq and PRes frames. The main part is used to check the validity of the current IO data.

---

## Mapping configuration

The mapping configuration defines how to map the received data into the application objects. This relation is defined in the objects 0x1600...0x16FF in subindices 1...255. Each of these subindices contains a mapping entry in the following format:

Object mapping interpretation of values (UNSIGNED64)					
No. of bits	63-48	47-32	31-24	23-16	15-0
Name	Length	Offset	Reserved	Sub index	Index
Data type	UNSIGNED16	UNSIGNED16		UNSIGNED8	UNSIGNED16

Table 11: Receive PDO mapping - Object mapping - Interpretation of values

- Index: index of object to be mapped from the receive data
- Subindex: subindex of object to be mapped from the receive data
- Offset: offset of the data into the IO data block (starting at the begin of the PDO area) to be mapped into the application object
- Length: size of mapped data (in Bits)

---

**Note:** Before configuring a PDO, this has to be disabled by setting the subindex 0 ("NumberOfEntries") of object 0x16XX to 0. If the object 0x14XX has to be changed too, its subindex 0 has also to be set to 0 at first. When the configuration is finished, set both subindices again to the correct value.

---



### 3.4.2 Configuring transmit PDO

Since an Ethernet POWERLINK Controlled Node only supports one single transmit PDO, the configuration can be made with the objects 0x1800 and 0x1A00 only. These objects contain the following information:

- Mapping version
- Mapping configuration

---

**Note:** The parameter NodeId (object 0x1800 subindex 1) is not used in Controlled Nodes, because the transmit data (PRes) are sent as Broadcast frames. The value has to be set to 0.

---

#### Mapping version

This parameter allows checking the validity of the current IO data with the configured mapping.

The mapping version is configured in the object 0x1800 in subindex 2.

This parameter is a byte where:

- Bits 4-7 contain the main version number
- Bits 0-3 contain the sub version number

---

**Note:** The mapping version is transmitted into PRes frames. The node receiving our data will check the main part of the version to ensure validity of the IO data.

---

#### Mapping configuration

The mapping configuration defines how to map the data from the application objects into the transmit data stream. This relation is defined in the object 0x1A00 in subindices 1...255. Each of these subindices contains a mapping entry in the following format:

Object mapping interpretation of values (UNSIGNED64)					
No. of bits	63-48	47-32	31-24	23-16	15-0
Name	Length	Offset	Reserved	Sub index	Index
Data type	UNSIGNED16	UNSIGNED16		UNSIGNED8	UNSIGNED16

Table 12: Transmit PDO mapping - Object mapping - Interpretation of values

- Index: index of object to be mapped in the transmit data
- Subindex: subindex of object to be mapped in the transmit data
- Offset: offset into the IO data image (starting at the begin of the PDO area) where the data have to be mapped
- Length: size of mapped data (in Bits)

---

**Note:** Before configuring a PDO, this has to be disabled by setting the subindex 0 ("NumberOfEntries") of object 0x1A00 to 0. If the object 0x1800 has to be changed too, its subindex 0 has also to be set to 0 at first. When the configuration is done, set both subindices again to the correct value.

---

## 3.5 Acyclic data communication / SDO

Acyclic data communication denominates the transfer of data between participants of the network which is not regularly or periodically repeated, but typically happening only once. Contrary to the situation of cyclic communication, this kind of communication usually deals with not extremely time-critical data or even data of low priority. Such communication is needed for purposes like commands which are to be executed only once, configuration, diagnosis or handling of emergency or error situations.

Within Ethernet Powerlink, acyclic data communication is done by Service Data Objects (SDOs). SDO communication is based on the client-server-model and is used to access the Object Dictionary of an EPL node.

### 3.5.1 SDO Server

The node receiving an SDO request is the server in the SDO communication.

Since Hilscher's stack uses a separate component for the Object Dictionary (ODV3), the SDO handling on application side in case of a SDO server is simplified to ODV3 handling. The SDO task of the stack receives the requests from the bus and routes them to the ODV3 component. This will send indications to the behavior of the requested object.

With this implementation, the application is abstracted from the SDO frame received from the bus and it has just to handle the ODV3 indications received for its objects.

**Note:** The application can receive the indications only for the objects created by itself and for which it has been registered. This means, all objects created by the stack will also be handled by the stack. For more information about creation of objects and registration for object access indications refer to reference 4 (document *Hilscher Gesellschaft für Systemautomation mbH: Protocol API, Object Dictionary*).

---

Lists of objects that are created automatically by the stack can be found in section *Object Dictionary entries (Communication Profile Area)*.

---

## 3.6 Diagnosis

The following diagnosis mechanisms are provided by the Ethernet POWERLINK Controlled Node protocol stack:

- Static Error Bit Field
- Status Entry
- Error Entry

More details about the diagnosis handling and the corresponding API can be found in section *Diagnosis* on page 89.

### 3.6.1 Static Error Bit Field

The Static Error Bit Field provides 64 bit flags with general diagnosis information. The first 8 bits contains the same information as the Error Register Object in the OD (Object 0x1001). The bits 8-15 are reserved and the rest of bits are vendor or profile specific.

### 3.6.2 Status Entry

The Status Entry is a vendor or profile specific entry to be written by the application.

### 3.6.3 Error Entry

Depending on its configuration, an Error Entry may contain a communication profile, a vendor specific or a device profile specific error. All errors detected by the stack itself are signaled using this mechanism (communication profile errors). All Error Entries are automatically logged in the Error History object in the OD (Object 0x1003). Additionally, the entries may be signaled in the Emergency Queue of the Status Response frame.

## 3.7 Commonly used values in packets

### 3.7.1 Values for identifying NMT states in packets

The following values are used for identifying NMT states in any field and relate to the current state or target state.

Value	Definition in API	Definition in EPSG
0x00	EPL_NMT_GS_OFF	NMT_GS_OFF
0x19	EPL_NMT_GS_INITIALISING	NMT_GS_INITIALISING
0x29	EPL_NMT_GS_RESET_APPLICATION	NMT_GS_RESET_APPLICATION
0x39	EPL_NMT_GS_RESET_COMMUNICATION	NMT_GS_RESET_COMMUNICATION
0x79	EPL_NMT_GS_RESET_CONFIGURATION	NMT_GS_RESET_CONFIGURATION
0x1C	EPL_NMT_CS_NOT_ACTIVE	NMT_CS_NOT_ACTIVE
0x1D	EPL_NMT_CS_PRE_OPERATIONAL_1	NMT_CS_PRE_OPERATIONAL_1
0x5D	EPL_NMT_CS_PRE_OPERATIONAL_2	NMT_CS_PRE_OPERATIONAL_2
0x6D	EPL_NMT_CS_READY_TO_OPERATE	NMT_CS_READY_TO_OPERATE
0xFD	EPL_NMT_CS_OPERATIONAL	NMT_CS_OPERATIONAL
0x4D	EPL_NMT_CS_STOPPED	NMT_CS_STOPPED
0x1E	EPL_NMT_CS_BASIC_ETHERNET	NMT_CS_BASIC_ETHERNET

Table 13: Meaning of *bCurrentState* and *bTargetState*

## 3.8 Multiplexing

The protocol stack supports multiplexing according to specification DS301 V1.3.0. The aim of multiplexing is to optimize the bandwidth in the isochronous phase by reducing the amount of data processed within a single cycle.

Multiplexed operation differs from continuous operation as follows:

- In continuous operation, the Managing Node accesses the Controlled Node in every cycle.
- In multiplexed operation the Managing Node accesses the Controlled Node only in every n-th cycle where n is a multiple of the basic cycle.

This approach is especially adequate in case of various Controlled Nodes within the network having different demands concerning the frequency of providing feedback.

For the application, no configuration is required. In order to support multiplexing, the protocol stack provides the objects 0x1F81 (Node assignment) and 0x1F9B (Multiple cycle assignment), see *Table 10: Communication Profile - General Overview*. To configure multiplexing, the Managing Node has to set some values within these two objects 0x1F81 and 0x1F9B, instead.

Beginning with version 3.4.0, the protocol stack indicates its multiplexing capability by setting bit D9 in the feature flags to TRUE, see subsection Feature flags (page 47) for more information.

## 3.9 NodeID assignment

Each node in a POWERLINK network is addressed with a unique NodeID. The NodeID may be set by hardware settings or by software.

### 3.9.1 Setting NodeID by hardware

Setting the NodeID by hardware is the default way to set a NodeID to a POWERLINK node. The application gets the NodeID value from the hardware (e.g. DIP switch, database, etc.) and sets the NodeID to the stack using the service

- SetConfig (section *Configure Stack service* on page 49) or
- SetNodeID (section *Configure NodeId* on page 70).

From the view point of the POWERLINK network, the device has a fixed and unchangeable NodeID.

### 3.9.2 Setting NodeID by software

The following steps are required to use the function “Setting NodeID by software”.

#### Application has to enable “Setting NodeID by software”

In case, the NodeID has to be set via the network, the application has to enable this feature first and to set the flag `EPL_FEATURE_FLAGS_NODEID_SETUP_BY_SOFTWARE` (section *Feature flags* on page 47) in the parameter `ulFeatureFlags` (section *Configure Stack service* on page 49).

#### Application has to handle object 0x1F93:03

As soon as the application has enabled “Setting NodeID by software”, the stack sets the value of object 0x1F93:02 (NodeIDByHW) to false. The application has to create and implement the behavior of object 0x1F93:03 (Snooded). The application has to register for write and read indications (mandatory) for this object to avoid the rejection of SDO access to the object. After a new NodeID has been received from the network, the application has to provide it to the stack using the SetNodeID service (section *Configure NodeId* on page 70).

### 3.10 Synchronous DPM IO exchange

The Powerlink Stack V3.5 allows configuration of the DPM IO exchange modes (free-run and synchronous) for both data directions (Tx and Rx).

Since the Powerlink HAL does not provide any sending confirmation for the TX data (PRes), only the RX data (PReq) can be used to synchronize both (PDin and PDout) DPM handshakes. This limitation also applies in case of a device running with enabled Multiplexing or Cross Traffic features.

#### Configuration

The flag `MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_DISABLE_HOST_TRIGGERED_PREQ_XCHG` formerly used for stack configuration is now deprecated and ignored by the stack beginning by V3.5.0.x. The application should not use it anymore.

To configure the IO exchange modes, the application has to use the Hilscher generic service `HIL_SET_TRIGGER_TYPE_REQ` with following content. This service needs to be supplied with three parameters for controlling the triggering of the handshake, namely `usPdInHskTriggerType`, `usPdOutHskTriggerType` and `usSyncHskTriggerType`.

Parameter `usPdInHskTriggerType`:

Value	Description
<code>HIL_TRIGGER_TYPE_PDIN_NONE</code>	Free-run mode. PReq data are exchanged using an internal buffer. The application gets the newest data received from the network
<code>HIL_TRIGGER_TYPE_PDIN_RX_DATA_RECEIVED</code>	Handshake coupled to PReq data receive. If the handshake is still locked by the application when receiving the PReq frame, the data are just ignored and the internal synchronous error counter is incremented
<code>HIL_TRIGGER_TYPE_PDIN_TIMED_ACTIVATION</code>	Not supported

Table 14: Possible values for parameter `usPdInHskTriggerType`

Parameter `usPdOutHskTriggerType`:

Value	Description
<code>HIL_TRIGGER_TYPE_PDOUT_NONE</code>	Free-run mode. The application writes the PRes data into the DPM in an own cycle. The stack will always copy the newest data into the buffer and return the handshake directly.
<code>HIL_TRIGGER_TYPE_PDOUT_READY_FOR_TX_DATA</code>	Handshake coupled to PReq data receive. The application writes the PRes data into the DPM, the stack processes the data but does not return the handshake until the next PReq frame was received.
<code>HIL_TRIGGER_TYPE_PDOUT_TIMED_LATCH</code>	Not supported

Table 15: Possible values for parameter `usPdOutHskTriggerType`

Parameter `usSyncHskTriggerType`:

Value	Description
<code>HIL_TRIGGER_TYPE_SYNC_NONE</code>	No sync signal
Others	Not supported

Table 16: Possible values for parameter `usSyncHskTriggerType`

### 3.11 Including the IO data status into the DPM

In the default configuration the Powerlink stack does not include the IO data status in the DPM.

In this mode, the following rules apply:

- If the received PReq data are invalid, the stack just zeroes them previous to copying these to the DPM.
- The PRes data are permanently marked as valid after the first DPM IO exchange.

---

**Note:** In Powerlink V3 ( $\geq 3.5$ ) and V5, the stack configuration flag `MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_USE_APP_READY_FOR_PRES_RD_FLAG` (Bit D4) is not supported anymore.

---

Beginning with V3.5, the application may now enable the IO data status in DPM, too. If the feature is enabled, all the rules mentioned above are not valid anymore.<sup>^</sup>

#### Enabling the IO data status within the DPM

To enable the IO data status in the dual-port memory, the configuration flag `MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_ENABLE_PDO_STATUS` has to be set in the SetConfig packet, see section Stack Configuration Flags on page 45. This activates the feature for both data flow directions (Rx and Tx). There is no way to enable them separately. If this feature is enabled, an additional byte is placed at the IO data containing the status information. The application and the protocol stack will read and write to the corresponding offset to provide IO data status information.

Use the following values to store status information:

- 0 → invalid
- 1 → valid.

All other values are reserved.



## 3.12 Cross-traffic

Beginning with V3.5, the protocol stack supports the cross traffic feature. This means, the protocol stack allows communication between any two controlled nodes, where each controlled node listens the PRes data of other controlled nodes in the Powerlink network. Similarly, the cross-traffic mechanism can also access the PRes data of managing nodes.

- The LFW version of the Powerlink stack supports up to 8 cross traffic channels.
- The LOM version of the Powerlink stack supports up to 254 cross traffic channels.

### Preconditions for cross-traffic

The following preconditions have to be fulfilled before the communication can be started:

1. The application has to be registered for stack indications. See section *Register for Status Indications service* on page 78.
2. Within the SetConfig packet (see section *Configure Stack service* on page 49), the following flags need to be set :

Parameter of SetConfig packet	Name of flag to be set
ulSystemFlags	MSK_EPLCN_IF_CFG_SYSTEM_FLAGS_APP_CONTROLLED
ulStackCfgFlags	MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_USE_CUSTOM_PDO_OBJ
	MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_SUPPORT_CROSS_TRAFFIC
ulFeatureFlags	EPL_FEATURE_FLAGS_DYNAMIC_PDO_MAPPING

3. After performing a `ChannelInit`, the application creates its PDO object dictionary (N = number of cross traffic channels to be supported). The following objects must be created according to the Powerlink specification:
  - Objects 0x1400 + 0x1401-0x140N
  - Objects 0x1600 + 0x1601-0x160N
  - Object 0x1800
  - Object 0x1A00
  - Application PDO objects (e.g. 0x2000 , 0x2100)

**Note:** The XDD file of the device has to contain all these objects mentioned above.

4. The stack will create the object 0x1F8D by itself. This object will contain the maximum number of bytes available for all cross traffic channels together.
5. The user configures the Managing Node in order to enable and configure the cross traffic feature there. Subsection Configuration of the Managing Node for enabling and configuring the cross traffic feature on page 43 describes how to do this.

If all of these conditions are fulfilled and the new configuration is loaded in the MN, the master sends the new PDO mapping configuration to the device.

In order to establish cross-traffic, the application must correctly react to two indications it will receive:

1. The application receives a write indication with the new configuration:

- Objects 0x1400, 0x1600, 0x1800 and 0x1A00 contain the normal PRes and PReq data mapping configuration of the device. The application has to follow the same steps as in usual dynamic mapping use-case described in this manual.
- Objects 0x140N and 0x160N contain information about cross traffic.
  - 0x140N contains the NodeID related to this cross traffic channel
  - 0x160N contains the mapping of the listening PRes data into application PDOs
  - See example below!

The following gives an example for an object containing information about cross traffic:

```
0x1402 NodeId = 5
0x1602 Mapping

    Object = 0x2000
    Subindex = 0x03
    Size = 0x08 (bits)
    Offset = 0x10 (bits)
```

In the PRes data listened from NodeID 5, get the data in offset 0x10 (bits) and with the length 0x08 (bits) and copy them to application object 0x2000, subindex 0x03

2. The application also receives the indication `EPLCN_IF_CROSS_TRAFFIC_CONFIGURED_IND`. This packet contains the information about the offset within the dual-port memory, where the stack will copy the PRes data of the different listening NodeIDs.

---

**Note:** The stack copies the complete PRes data into the dual-port memory, the application has to extract its cross traffic data depending on the mapping information received with the above mentioned indication.

---



---

**Note:** If the configuration flag `MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_ENABLE_PDO_STATUS` is also set, the data size provided in the indication packet already includes the additional single byte for the data status information. This byte will be located at the end of the data.

---



---

**Note:** If the synchronous DPM mode is activated for the Rx data, the handshake will be returned by the stack on the reception of the PReq frame. If the Controlled Node running the Hilscher protocol stack or the listening Controlled Nodes are running in multiplexed mode, the cross traffic data may originate from an older cycle.

---

### 3.12.1 Configuration of the Managing Node for enabling and configuring the cross traffic feature

The subsequent description roughly sketches how to enable and configure the cross traffic feature of the Powerlink Stack V3.5. To do so, follow the stepwise description given below:

1. Create a new configuration for the Managing Node with its administration tool, for instance with Automation Studio.
2. Add the Hilscher device running the Powerlink Controlled Node-Stack V3.5 to the configuration of the Managing Node.
  - To do so, create to new objects, one for direct communication (PReq from MN) and one for Cross Traffic (PRes from CN).
  - For test purposes, it might be useful, to trigger an action with visible consequences such as lighting a LED by this action.
  - You may freely choose the names of these objects.
3. Activate the mapping.
  - To do so, set the according channels in the IO configuration of the Hilscher device to "Write".
4. Add other CN(s) to the configuration of the Managing Node that provide PRes data
5. Open the IO Mapping Configuration window for the Hilscher device and connect the desired PDOs from the output channel of the CN(s) with the input channel of the Hilscher device.

## 4 Application interface

This chapter defines the application interface of the Ethernet POWERLINK Controlled Node stack.

The following service categories are supported:

- Configuring the Ethernet POWERLINK Controlled Node (page 44)
- NMT State control (page 73)
- Status indications (page 78)
- Diagnosis (page 89)

### 4.1 Configuring the Ethernet POWERLINK Controlled Node

This chapter explains the service and the procedure to configure the Ethernet POWERLINK Controlled Node stack.

The sections *Parameters used in all configuration variants* (page 45), *Common services (All packet configuration variants)* (page 49), and *Common configuration sequence* (page 56) describe the common configuration, which is used in all configuration variants. The rest of section provides specific details for each configuration variant.

The basic configuration method is explained in section *Static mapping configuration with default PDO objects* on page 59. This is the method to be used for applications with a defined static PDO mapping configuration, where the application does not care about the creation of PDO objects and the configuration of PDO mapping.

Applications with static PDO mapping, which need a full control of the PDO layout and mapping, should use the configuration variant described in *Static mapping configuration with user defined PDO objects* (page 62).

Applications with dynamic PDO mapping have to use the configuration method explained in *Dynamic mapping configuration* (page 64). This configuration variant provides a full control about the PDO configuration to the application. A default PDO mapping has to be defined at startup. The MN may request a change of this configuration. In this case, the application receives the corresponding indications and has to signal the new PDO configuration to the stack.

## 4.1.1 Parameters used in all configuration variants

### 4.1.1.1 Stack Configuration Flags

The following table provides an overview of the Stack Configuration Flags used in the configuration packet (see *Configure Stack service* on page 49):

Bit	Description
D7-31	Reserved Reserved, set to 0
D6	MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_SUPPORT_CROSS_TRAFFIC If set, the stack will manage slave-to-slave communication (cross traffic). See section <i>Cross-traffic</i> .
D5	MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_ENABLE_PDO_STATUS If set, the protocol stack automatically handles IO status information which is added directly following the process data. See section <i>Including the IO data status into the DPM</i> .
D4	MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_USE_APP_READY_FOR_PREP_RD_FLAG If set, the firmware uses the DPM Application Ready flag in the communication change of state register for the RD flag (Data valid) of the Poll Response frames. Otherwise, the firmware set the Poll Response data to valid only after the transmit data were exchanged the first time in the DPM. <b>Note:</b> Beginning with V3.5, this flag is no longer supported.
D3	MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_DISABLE_PDO_MAP_VERS_CHECK If set, the firmware does not check the mapping version received within the Poll Request data from the bus. Otherwise, the check is done and if a mismatch is detected, the corresponding communication error is signaled.
D2	MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_USE_CUSTOM_PDO_OBJ If set, the application creates and handles the PDO objects (Objects 0x1400, 0x1600, 0x1800, 0x1A00 and the user PDO objects) in Object Dictionary itself. Otherwise, the firmware creates the PDO objects automatically. <b>Note:</b> For dynamic mapping devices, this flag has to be set.
D1	MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_DISABLE_HOST_TRIGGERED_PREQ_XCHG <b>Flag obsolete since V3.5</b> This flag is no longer supported at all and no backward compatibility is provided. Instead of this flag, legacy applications should use the Hilscher Service <code>HIL_SET_TRIGGER_TYPE_REQ</code> , in order to configure synchronous operation.
D0	MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_NMT_TRIGGERED_BY_APP If set, some state switches in the NMT state machine will be triggered by the host application. See section <i>State machine (NMT)</i> on page 16 for more details about the application controlled state changes. Otherwise, the NMT state machine runs automatically.

Table 17: Value for Stack Configuration Flags

#### 4.1.1.2 Use of Custom Threshold Flags

The following table provides an overview of the values for the parameter `bUseCustomThreshold` in the configuration packet (see *Configure Stack service* on page 49):

Bit	Description
D5-31	Reserved Reserved, set to 0
D4	MSK_EPLCN_IF_CFG_USE_CUSTOM_TH_CRC_ERROR_THRESHOLD If set, the value of <code>ulThresholdCrcError</code> in the configuration packet will be used. Otherwise, the firmware uses the default value described in the Ethernet POWERLINK protocol.
D3	MSK_EPLCN_IF_CFG_USE_CUSTOM_TH_COLLISION_THRESHOLD If set, the value of <code>ulThresholdCollision</code> in the configuration packet will be used. Otherwise, the firmware uses the default value described in the Ethernet POWERLINK protocol.
D2	MSK_EPLCN_IF_CFG_USE_CUSTOM_TH_LOSS_SOA_THRESHOLD If set, the value of <code>ulThresholdLossSoA</code> in the configuration packet will be used. Otherwise, the firmware uses the default value described in the Ethernet POWERLINK protocol.
D1	MSK_EPLCN_IF_CFG_USE_CUSTOM_TH_LOSS_PREQ_THRESHOLD If set, the value of <code>ulThresholdLossPReq</code> in the configuration packet will be used. Otherwise, the firmware uses the default value described in the Ethernet POWERLINK protocol.
D0	MSK_EPLCN_IF_CFG_USE_CUSTOM_TH_LOSS_SOC_THRESHOLD If set, the value of <code>ulThresholdSoC</code> in the configuration packet will be used. Otherwise, the firmware uses the default value described in the Ethernet POWERLINK protocol.

Table 18: Value for use of Custom Threshold flags

### 4.1.1.3 Feature flags

The Ethernet POWERLINK Feature Flags (Object 0x18F2) describes the optional protocol functionalities supported by the node.

The following table provides a complete overview of the Feature Flags for the EPL communication profile. Some of the flags are set automatically by the stack, since these functionalities are always supported. Others may be set by the application, within the configuration data.

The table below explains the meaning and significance of the single bits of the feature flag set:

Bit	Description	Info
D17-31	Reserved	Reserved
D16	EPL_FEATURE_FLAGS_MULTIPLE_ASND TRUE: Extended feature Multiple ASND is supported. This indicates that the node is able to send multiple ASND frames in the same cycle if it is required by the MN. FALSE: Not supported.	The stack sets this flag automatically to TRUE, since this is always supported.
D11-15	Reserved	Reserved
D10	EPL_FEATURE_FLAGS_NODEID_SETUP_BY_SOFTWARE TRUE: NodeID setup by software is enabled. The stack sets the value of object 0x1F93:02 to 0 (false). The application has to create object 0x1F93:03 in order to receive the new NodeID value via SDO. FALSE: NodeID setup by hardware is enabled.	The application has to set this flag to TRUE to enable this feature. For more information, see section <i>Setting NodeID by software</i> on page 38.
D9	EPL_FEATURE_FLAGS_MULTIPLEXED_ACCESS TRUE: Device support multiplexed isochronous access and may be accessed with PReq frames by the master every n cycle. The activation and configuration of the multiplexed cycle is done by the MN. FALSE: Not supported	The stack sets this flag automatically to TRUE, since this is always supported.
D8	EPL_FEATURE_FLAGS_CONFIGURATION_MANAGER TRUE: Device implements a configuration manager for the storage of configuration in non-volatile memory. (For more information about this feature see section 6.7 "Configuration Management" of reference 3. FALSE: Not supported.	Application must set this flag if implementing a configuration manager.
D7	EPL_FEATURE_FLAGS_NMT_SERVICE_VIA_UDP TRUE: Device supports NMT services (NMT commands) via UDP/IP (NMT services via POWERLINK frames are standard). FALSE: Not supported	Stack set this flag automatically to TRUE, since this is always supported.
D6	EPL_FEATURE_FLAGS_DYNAMIC_PDO_MAPPING TRUE: Device supports dynamic PDO mapping. FALSE: Not supported.	Application must set this flag if implementing a device with dynamic PDO mapping.
D5	EPL_FEATURE_FLAGS_EXTENDED_NMT_STATE_COMMANDS TRUE: Device supports NMT extended state commands. This is used by the MN to address a group of nodes with the same NMT state command. FALSE: Not supported	Stack set this flag automatically to TRUE, since this is always supported.
D3-4	Reserved	Reserved
D2	EPL_FEATURE_FLAGS_SDO_VIA_ASND TRUE: Device supports SDO communication via POWERLINK ASnd frames. FALSE: Not supported	Stack set this flag automatically to TRUE, since this is always supported.
D1	EPL_FEATURE_FLAGS_SDO_VIA_UDP TRUE: Device supports SDO communication via UDP/IP frames. FALSE: Not supported	Stack set this flag automatically to TRUE, since this is always supported.

Bit	Description	Info
D0	<p>EPL_FEATURE_FLAGS_ISOCHRONOUS</p> <p>TRUE: Device supports cyclic IO communication and may be accessed isochronously via PReq.</p> <p>FALSE: Asynchronous device without cyclic IO communication. Only acyclic communication is allowed.</p>	Stack set this flag automatically to TRUE, since this is always supported.

*Table 19: Meaning of the Feature Flags*



## 4.1.2 Common services (All packet configuration variants)

### 4.1.2.1 Configure Stack service

This service has to be used by the host application when configuring the stack using the packet interface. This packet is part of the basic packet set and is used in all configuration variants.

The following rules apply for the behavior of the Ethernet POWERLINK Controlled Node stack when receiving the `EPLCN_IF_SET_CONFIG_REQ` command:

- The configuration data is checked for consistency and integrity.
- In case of failure, no data is accepted and the packet is returned with error code
- In case of success, the configuration data are stored internally (within the RAM).
- The new configuration data will be activated only after a channel init request (`RCX_CHANNEL_INIT_REQ` / command code 0x2F80).
- This packet does not perform any registration at the stack automatically. Details for registration of the application can be found in section *Registration and deregistration of Status Indications* on page 78.

## Packet structure reference

```

/* System flags */
#define MSK_EPLCN_IF_CFG_SYSTEM_FLAGS_APP_CONTROLLED 0x00000001

/* Stack configuration flags */
#define MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_NMT_TRIGGERED_BY_APP 0x00000001
#define MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_DISABLE_HOST_TRIGGERED_PREQ_XCHG 0x00000002
#define MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_USE_CUSTOM_PDO_OBJ 0x00000004
#define MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_DISABLE_PDO_MAP_VERS_CHECK 0x00000008
#define MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_USE_APP_READY_FOR_PRES_RD_FLAG 0x00000010

/* Custom threshold for loss of frames detection */
#define MSK_EPLCN_IF_CFG_USE_CUSTOM_TH_LOSS_SOC_THRESHOLD 0x00000001
#define MSK_EPLCN_IF_CFG_USE_CUSTOM_TH_LOSS_PREQ_THRESHOLD 0x00000002
#define MSK_EPLCN_IF_CFG_USE_CUSTOM_TH_LOSS_SOA_THRESHOLD 0x00000004
#define MSK_EPLCN_IF_CFG_USE_CUSTOM_TH_COLLISION_THRESHOLD 0x00000008
#define MSK_EPLCN_IF_CFG_USE_CUSTOM_TH_CRC_ERROR_THRESHOLD 0x00000010
#define MSK_EPLCN_IF_CFG_USE_CUSTOM_TH_SOC_JITTER_THRESHOLD 0x00000020

typedef struct EPLCN_IF_SET_CONFIG_REQ_DATA_Ttag
{
    TLR_UINT32      ulSystemFlags;
    TLR_UINT32      ulWatchdogTime;
    TLR_UINT32      ulStackCfgFlags;
    TLR_UINT32      ulVendorId;
    TLR_UINT32      ulProductCode;
    TLR_UINT32      ulRevisionNumber;
    TLR_UINT32      ulSerialNumber;
    TLR_UINT32      ulCycleLength;
    TLR_UINT32      ulDeviceType;
    TLR_UINT32      ulFeatureFlags;
    TLR_UINT16      usPReqDataSize;
    TLR_UINT16      usPResDataSize;
    TLR_UINT8       bPReqMappingVersion;
    TLR_UINT8       bPResMappingVersion;
    TLR_UINT16      usMaxPReqDataSize;
    TLR_UINT16      usMaxPResDataSize;
    TLR_UINT8       bNodeId;
    TLR_UINT32      ulGatewayAddress;
    TLR_UINT8       abNodeName[32];
    TLR_UINT8       bNumberOfStatusEntries;
    TLR_UINT8       bUseCustomThreshold;
    TLR_UINT32      ulThresholdLossSoC;
    TLR_UINT32      ulThresholdLossPReq;
    TLR_UINT32      ulThresholdLossSoA;
    TLR_UINT32      ulThresholdCollision;
    TLR_UINT32      ulThresholdCrcError;
    TLR_UINT32      ulMinCycleLength;
    TLR_UINT32      ulThresholdSoCJitter;

    TLR_UINT32      aulReserved[8];
} EPLCN_IF_SET_CONFIG_REQ_DATA_T;

typedef struct EPLCN_IF_SET_CONFIG_REQ_Ttag
{
    TLR_PACKET_HEADER_T      tHead;
    EPLCN_IF_SET_CONFIG_REQ_DATA_T  tData;
} EPLCN_IF_SET_CONFIG_REQ_T;

```

**Packet description**

Structure EPLCN_IF_SET_CONFIG_REQ_T			Type: Request
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	149	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA230	EPLCN_IF_SET_CONFIG_REQ - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch
<b>tData - Structure EPLCN_IF_SET_CONFIG_REQ_DATA_T</b>			
ulSystemFlags	UINT32 (Bit field)	0, 1 Default: 0	System flags area The start of the device can be performed either application controlled or automatically: Automatic (0): Communication with the MN after device start is allowed without "BUS ON". But the communication will be interrupted if the "BUS ON" flag changes state to 0 Application controlled (1): The channel firmware is forced to wait for the Application Ready flag in the communication change of state register to be set (see section 3.2.5.1 of reference 1). Communication with the MN is only allowed with the "BUS ON" flag. For more information concerning this topic see section 4.4.1 "Controlled or Automatic Start" of reference 1.
ulWatchdogTime	UINT32	0, 20..65535 Default: 1000	DPM Watchdog time (in milliseconds). 0 = Watchdog timer has been switched off
ulStackCfgFlags	UINT32 (Bit field)	0.. $2^{32}-1$ Default: 0	Stack Configuration flags. See subsection <i>Stack Configuration Flags</i> on page 45 for details about this parameter.
ulVendorId	UINT32	0.. $2^{32}-1$	Vendor identification. This is an identification number for the manufacturer of an Ethernet POWERLINK device. Vendor IDs are managed by the EPSG (see <a href="http://www.ethernet-powerlink.org">http://www.ethernet-powerlink.org</a> ) This parameter corresponds to object 0x1018 subindex 1 of the Object Dictionary
ulProductCode	UINT32	0.. $2^{32}-1$	Product Code of the device. This is a manufacturer specific ID for the device. This parameter corresponds to object 0x1018 subindex 2 of the Object Dictionary
ulRevisionNumber	UINT32	0.. $2^{32}-1$	Revision number of the device. This parameter corresponds to object 0x1018 subindex 3 of the Object Dictionary

Structure EPLCN_IF_SET_CONFIG_REQ_T			Type: Request
ulSerialNumber	UINT32	0..2 <sup>32</sup> -1	Serial number of the device. 0 = Firmware set the serial number stored in the security memory or flash device label (If available) This parameter corresponds to object 0x1018 subindex 4 of the Object Dictionary
ulCycleLength	UINT32	Default: 1000	Default communication cycle time interval in microseconds. Possible value range is limited by the value of NMTCycleTimeMin and NMTCycleTimeMax in the decryption file (XDD) of the device. See section <i>Device description file (XDD)</i> on page 111 for more information. 0 = Stack ignores this parameter and set the default value. This value may be changed by the MN. The default value is activated again if the device returns to NMT_GS_RESET_COMMUNICATION state This parameter corresponds to object 0x1006 of the Object Dictionary.
ulDeviceType	UINT32	0..2 <sup>32</sup> -1 Default: 0	Device Type of the device. This parameter describes the type of device and its functionality. LSB = Device Profile Number (0 = no standardized device) MSB = Additional Information This parameter corresponds to object 0x1000 of the Object Dictionary. See reference 3, chapter 7.2.1.1.1 for more information about this parameter.
ulFeatureFlags	UINT32	0..2 <sup>32</sup> -1	Feature flags of the device. See subsection <i>Feature flags</i> on page 47 for details about this parameter. This Feature Flags appears also in the object 0x1F82 of the Object Dictionary
usPReqDataSize	UINT16	0..1490	Poll Request data size (Receive data). For static mapping device, this parameter defines the fix size of the receive data. For dynamic mapping device, this parameter defines the default size of the receive data. In this case, the value may be changed by the application or the MN depending on new IO configuration.
usPResDataSize	UINT16	0..1490	Poll Response data size (Transmit data). For static mapping device, this parameter defines the fix size of the transmit data. For dynamic mapping device, this parameter defines the default size of the transmit data. In this case, the value may be changed by the application or the MN depending on new IO configuration.
bPReqMappingVersion	UINT8	0..255	Mapping version of the Poll Request mapping configuration. For static mapping device, this parameter defines the fix mapping configuration version of the receive data. For dynamic mapping device, this parameter defines the version of the default mapping configuration for the receive data. This may be changed, if a new mapping is configured.
bPResMappingVersion	UINT8	0..255	Mapping version of the Poll Response mapping configuration. For static mapping device, this parameter defines the fix mapping configuration version of the transmit data. For dynamic mapping device, this parameter defines the version of the default mapping configuration for the transmit data. This may be changed, if a new mapping is configured.

Structure EPLCN_IF_SET_CONFIG_REQ_T			Type: Request
usMaxPReqDataSize	UINT16	0..1490	Maximum Poll Request data size supported by the device. This parameter defines the upper limit for the value of usPReqDataSize
usMaxPResDataSize	UINT16	0..1490	Maximum Poll Response data size supported by the device. This parameter defines the upper limit for the value of usPResDataSize
bNodeId	UINT8	1..239	Node ID of the device. This parameter defines the node address of the device. The IP address of the device derives from this value as following: 192.168.100.x, with x = bNodeId
ulGatewayAddress	UINT32	192.168.100.1..192.168.100.254 Default: 192.168.100.254	Default value of the gateway address for the IP communication. 0 = Stack ignores this parameter and set the default value. This value may be changed by the MN. The default value is activated again if the device returns to NMT_GS_RESET_COMMUNICATION state.
abNodeName[32]	UINT8[]		DNS host name of the device for the IP communication. This value may be changed by the MN. The default value is activated again if the device returns to NMT_GS_RESET_COMMUNICATION state. This parameter corresponds to object 0x1F9A of the Object Dictionary.
bNumberOfStatusEntries	UINT8	0..13 Default: 0	Number of Status Entries supported by the device.
bUseCustomThreshold	UINT8 (Bit field)	0..31 Default: 0	Bit flags to define whenever the application defines the threshold level on its own. If the flag of a threshold is set, the corresponding threshold value in the configuration will be set. If the flag of a threshold is not set, the corresponding threshold value in the configuration will be ignored and the stack will set the default value instead.
ulThresholdLossSoC	UINT32	0..2 <sup>32</sup> -1 Default: 15	Threshold for Loss of SoC. 0 = Threshold is deactivated This value may be changed by the MN. The default value is activated again if the device returns to NMT_GS_RESET_COMMUNICATION state. More information about the thresholds can be found in section <i>Error detection</i> on page 89. This parameter corresponds to object 0x1C0B subindex 3 of the Object Dictionary
ulThresholdLossPReq	UINT32	0..2 <sup>32</sup> -1 Default: 15	Threshold for Loss of PReq. 0 = Threshold is deactivated This value may be changed by the MN. The default value is activated again if the device returns to NMT_GS_RESET_COMMUNICATION state More information about the thresholds can be found in section <i>Error detection</i> on page 89. This parameter corresponds to object 0x1C0D subindex 3 of the Object Dictionary

Structure EPLCN_IF_SET_CONFIG_REQ_T			Type: Request
ulThresholdLossSoA	UINT32	0..2 <sup>32</sup> -1 Default: 15	<p>Threshold for Loss of SoA.</p> <p>0 = Threshold is deactivated</p> <p>This value may be changed by the MN. The default value is activated again if the device returns to NMT_GS_RESET_COMMUNICATION state.</p> <p>More information about the thresholds can be found in section <i>Error detection</i> on page 89.</p> <p>This parameter corresponds to object 0x1C0C subindex 3 of the Object Dictionary.</p>
ulThresholdCollision	UINT32	0..2 <sup>32</sup> -1 Default: 15	<p>Threshold for Collision errors.</p> <p>0 = Threshold is deactivated</p> <p>This value may be changed by the MN. The default value is activated again if the device returns to NMT_GS_RESET_COMMUNICATION state.</p> <p>More information about the thresholds can be found in section <i>Error detection</i> on page 89.</p> <p>This parameter corresponds to object 0x1C0A subindex 3 of the Object Dictionary.</p>
ulThresholdCrcError	UINT32	0..2 <sup>32</sup> -1 Default: 15	<p>Threshold for CRC errors.</p> <p>0 = Threshold is deactivated</p> <p>This value may be changed by the MN. The default value is activated again if the device returns to NMT_GS_RESET_COMMUNICATION state.</p> <p>More information about the thresholds can be found in section <i>Error detection</i> on page 89.</p> <p>This parameter corresponds to object 0x1C0F subindex 3 of the Object Dictionary.</p>
ulMinCycleLength	UINT32	Default: 0	<p>Minimum cycle length supported by the device in microseconds. This parameter corresponds to the XDD file entry NMTCycleTimeMin. The value is used to check the value range of the parameter ulCycleLength.</p> <p>This parameter allows the user to redefine its own minimum cycle length. The new minimum cycle has to equal or greater than the hardware specific minimum cycle time:</p> <p>netX100/500: 200us</p> <p>netX51/52: 200us</p> <p>If the value 0 is configured, the parameter will be ignored and the stack will be configured with the hardware specific values.</p>
ulThresholdSoCJitter	UINT32	0..2 <sup>32</sup> -1 Default: 15	<p>Threshold for SoC Jitter errors.</p> <p>0 = Threshold is deactivated</p> <p>This value may be changed by the MN. The default value is activated again if the device returns to NMT_GS_RESET_COMMUNICATION state.</p> <p>More information about the thresholds can be found in section <i>Error detection</i> on page 89.</p> <p>This parameter corresponds to object 0x1C0E subindex 3 of the Object Dictionary.</p>
aulReserved[8]	UINT32[]		Reserved.

Table 20: EPLCN\_IF\_SET\_CONFIG\_REQ – Configure Stack Request

## Packet structure reference

```
typedef struct EPLCN_IF_SET_CONFIG_CNF_Ttag
{
    TLR_PACKET_HEADER_T          tHead;
} EPLCN_IF_SET_CONFIG_CNF_T;
```

## Packet description

Structure EPLCN_IF_SET_CONFIG_CNF_T			Type: Confirmation
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	0	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA231	EPLCN_IF_SET_CONFIG_CNF - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch

Table 21: EPLCN\_IF\_SET\_CONFIG\_CNF – Configure Stack Confirmation

### 4.1.3 Common configuration sequence

This chapter provides an overview of the common configuration sequence, which is used by all configuration variants. This sequence depends on the start modus of the stack, Auto-Start or Start-by-Application (see *Configure Stack service* on page 49).

In the Auto-Start modus, the stack starts automatically after a Channel Init is performed. Since the stack resets the objects in OD after each Channel Init, this modus can only be used for the simplest configuration variant (Static mapping configuration with default PDO objects and without creation of any other user objects) or in case the application registers for all undefined objects (see reference 4 - Object Dictionary V3 manual).

In case the application creates own objects, the Start-by-Application mode should be used and the application has to create its objects between the ChannelInit and the BusOn services.



### 4.1.3.1 Configuration sequence with Auto-start mode

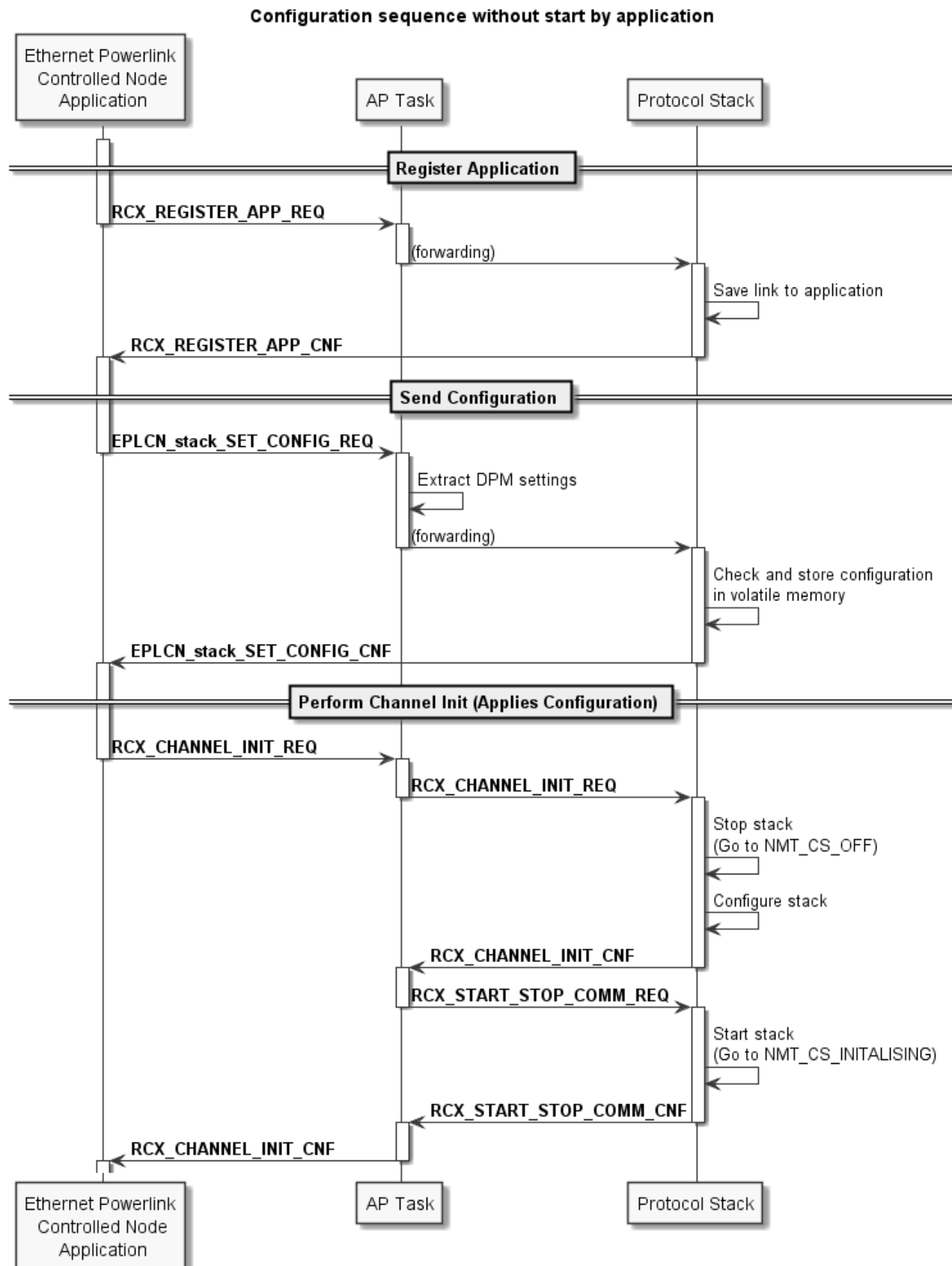


Figure 4: Configuration sequence with Auto-start mode

### 4.1.3.2 Configuration sequence with Start-by-Application mode

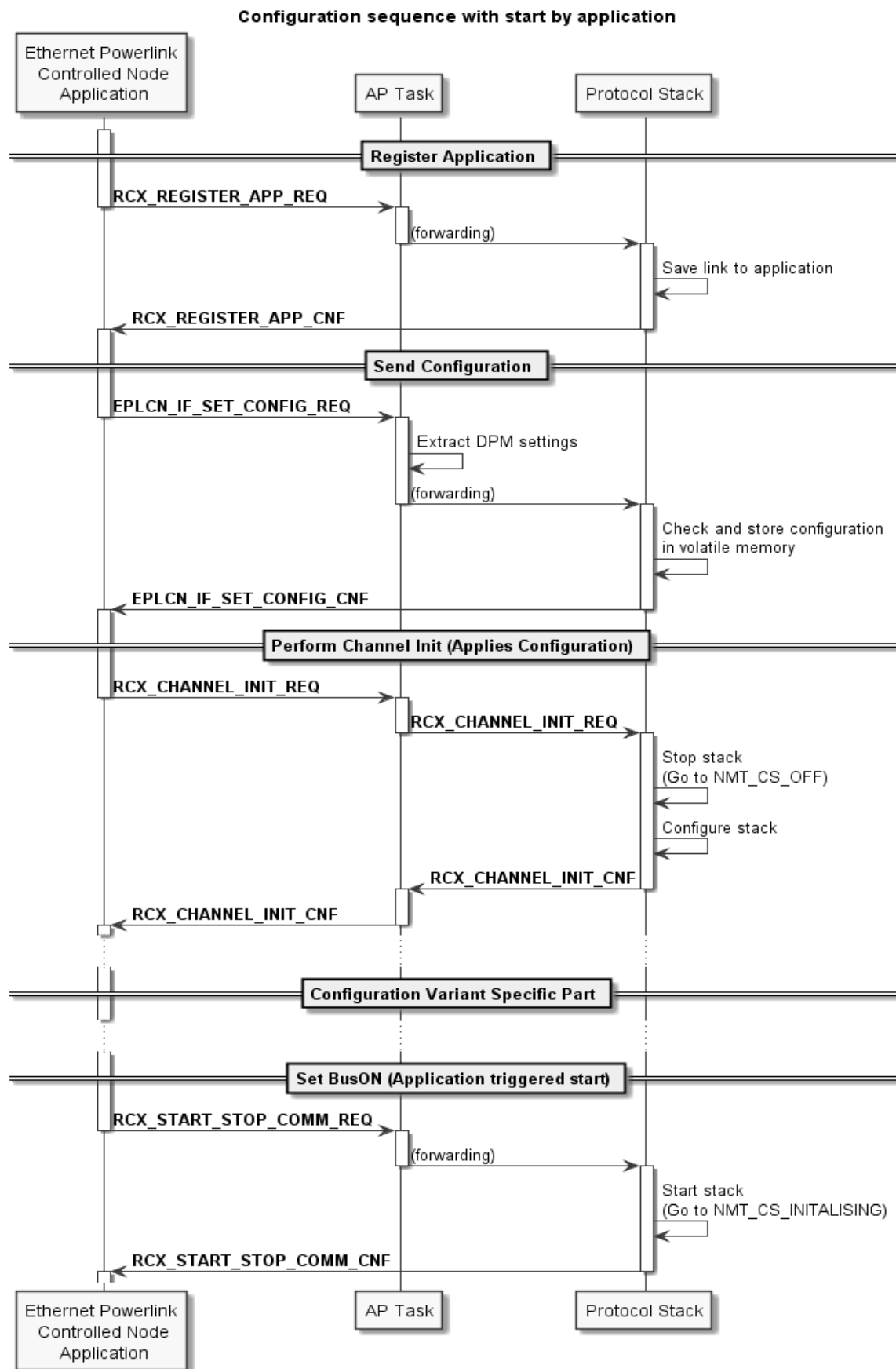


Figure 5: Configuration Sequence with Start-by-Application Mode

## 4.1.4 Static mapping configuration with default PDO objects

For this configuration variant, only the `EPLCN_IF_SET_CONFIG_REQ` service is needed. Additionally, after performing the Channel Init, the application may create its own objects using the ODV3 interface.

The following sections describe the flags configuration needed for this variant and show the variant's specific configuration sequence. The last section provides an overview about the mechanism, how the stack creates the default PDO objects and defines the mapping configuration.

### 4.1.4.1 Stack Configuration Flags

In the following table, only the parameters with a fixed value for this configuration variant are displayed. Other parameters follow the common rules.

Bit	Description	Value
D2	MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_USE_CUSTOM_PDO_OBJ	FALSE

Table 22: Stack Configuration Flags for static mapping configuration with default PDO objects

### 4.1.4.2 Feature Flags

In the following table, only the parameters with a fixed value for this configuration variant are displayed. Other parameters follow the common rules.

Bit	Description	Value
D6	EPL_FEATURE_FLAGS_DYNAMIC_PDO_MAPPING	FALSE

Table 23: Feature Flags for static mapping configuration with default PDO objects

#### 4.1.4.3 Configuration sequence

If no user objects have to be created, the application may use the configuration sequence described in section *Configuration sequence with Auto-start mode* on page 57. Otherwise, the application has to use the sequence from section *Configuration sequence with Start-by-Application mode* on page 58 within following sequence for the configuration variant specific part:

##### Configuration sequence for static mapping with default PDO objects

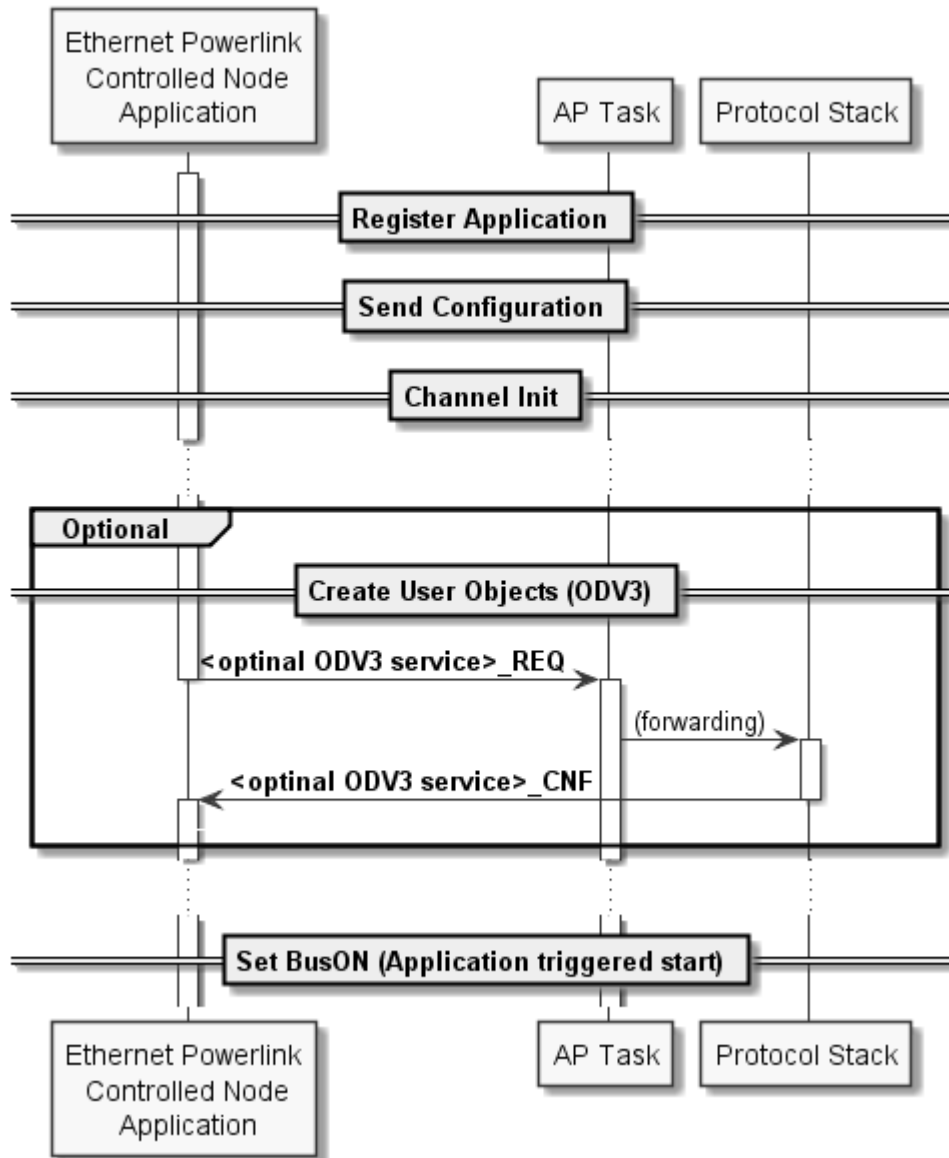


Figure 6: Sequence of static mapping configuration with default PDO objects

#### 4.1.4.4 PDO objects configuration

After the configuration is received, the stack creates default PDO objects and the corresponding mapping objects according the following rules:

- Receive data (Poll Request):
  - The object 0x2000 is created for the cyclic data. The data total bytes are grouped in the minimum subindices of type UNSIGNED64, UNSIGNED32 and UNSIGNED8.
  - The object 0x1600 is created for the mapping configuration. Each subindex of this object maps to the same subindex of the object 0x2000.
  - The object 0x1400 is created for the mapping version information. The mapping version is set to 0.
- Transmit data (Poll Response):
  - The object 0x2100 is created for the cyclic data. The data total bytes are grouped in the minimum subindices of type UNSIGNED64, UNSIGNED32 and UNSIGNED8.
  - The object 0x1A00 is created for the mapping configuration. Each subindex of this object maps to the same subindex of the object 0x2100.
  - The object 0x1800 is created for the mapping version information. The mapping version is set to 0.

#### Example

Configuring stack with 13 Bytes PReq and 9 Bytes PRes:

Poll request configuration		
Object	Subindex	Description
0x2000	1	Data type UNSIGNED64. Here the bytes 0...7 of the PReq data are included.
	2	Data type UNSIGNED32. Here the bytes 8...11 of the PReq data are included.
	3	Data type UNSIGNED8. Here is the byte 12 of the PReq data.
0x1600	1	Maps 8 bytes of the PReq data starting by offset 0 to the subindex 1 of 0x2000
	2	Maps 4 bytes of the PReq data starting by offset 8 to the subindex 2 of 0x2000
	3	Maps 1 byte of the PReq data starting by offset 12 to the subindex 3 of 0x2000
0x1400	1	Set to 0.
	2	Set to 0.
Poll response configuration		
Object	Subindex	Description
0x2100	1	Data type UNSIGNED64. Here the bytes 0..7 of the PRes data are included
	2	Data type UNSIGNED8. Here is the byte 8 of the PRes data.
0x1A00	1	Maps 8 bytes of the PRes data starting by offset 0 to the subindex 1 of 0x2100
	2	Maps 1 byte of the PRes data starting by offset 8 to the subindex 2 of 0x2100
0x1800	1	Set to 0.
	2	Set to 0.

Table 24: Example of PDO default objects configuration

## 4.1.5 Static mapping configuration with user defined PDO objects

For this configuration variant, additionally to the basic service `EPLCN_IF_SET_CONFIG_REQ`, the user application has to create the complete PDO configuration objects (0x1400, 0x1600, 0x1800, 0x1A00 and the data objects). The application may also create additional user specific objects. Please refer to reference 4 (Object Dictionary V3 manual) for more details about the creation of objects.

The following sections describe the flags configuration needed for this variant and show the variant's specific configuration sequence.

### 4.1.5.1 Stack Configuration Flags

In the following table, only the parameters with a fixed value for this configuration variant are displayed. Other parameters follow the common rules.

Bit	Description	Value
D2	MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_USE_CUSTOM_PDO_OBJ	TRUE

Table 25: Stack Configuration Flags for static mapping configuration with user defined PDO objects

### 4.1.5.2 Feature Flags

In the following table, only the parameters with a fixed value for this configuration variant are displayed. Other parameters follow the common rules.

Bit	Description	Value
D6	EPL_FEATURE_FLAGS_DYNAMIC_PDO_MAPPING	FALSE

Table 26: Feature Flags for static mapping configuration with user defined PDO objects

### 4.1.5.3 Configuration sequence

For this configuration variant the sequence described in section *Configuration sequence with Start-by-Application mode* on page 58 within following variant-specific sequence has to be implemented.

#### Configuration sequence for static mapping with user PDO objects

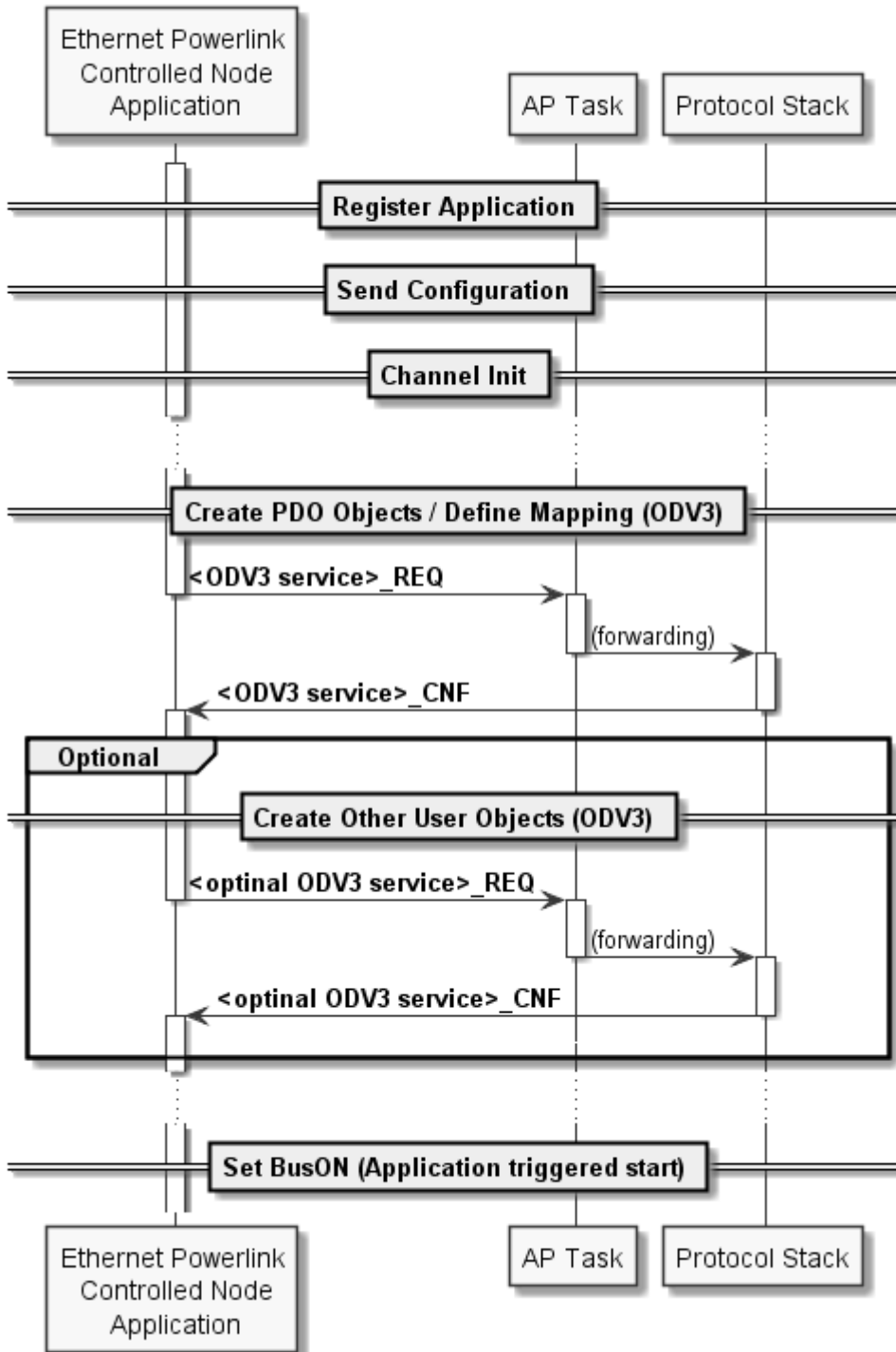


Figure 7: Sequence of static mapping configuration with user defined PDO objects

### 4.1.6 Dynamic mapping configuration

For this configuration variant, additionally to the basic service `EPLCN_IF_SET_CONFIG_REQ`, the user application has to create the default PDO configuration objects (0x1400, 0x1600, 0x1800, 0x1A00 and the data objects). The application may also create additional user specific objects. Please refer to Object Dictionary V3 manual (reference [4]) for more details about the creation of objects.

The default PDO configuration may be changed from the bus. If this happens, the application gets ODV3 specific indications for the write access. After the new mapping configuration is received, the application is now able to transmit the new PDO size and mapping version to the stack by using the `EPLCN_IF_SET_PDO_SIZE_REQ` service.

The following sections describe the flags configuration needed for this variant and show the variant's specific configuration sequence. Additionally, the sections *Set PDO size service* (page 66) and *Set PDO Size sequence* (page 69) provide detailed description of the `EPLCN_IF_SET_PDO_SIZE_REQ` service within its sequence diagram.

#### 4.1.6.1 Stack Configuration Flags

In the following table, only the parameters with a fixed value for this configuration variant are displayed. Other parameters follow the common rules.

Bit	Description	Value
D2	MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_USE_CUSTOM_PDO_OBJ	TRUE

Table 27: Stack Configuration Flags for dynamic mapping configuration

If cross traffic is used, the following stack configuration flag must also be set:

Bit	Description	Value
D6	MSK_EPLCN_IF_CFG_STACK_CFG_FLAGS_SUPPORT_CROSS_TRAFFIC	TRUE

Table 28: Additional Stack Configuration Flag for dynamic mapping configuration with cross traffic

#### 4.1.6.2 Feature Flags

In the following table, only the parameters with a fixed value for this configuration variant are displayed. Other parameters follow the common rules.

Bit	Description	Value
D6	EPL_FEATURE_FLAGS_DYNAMIC_PDO_MAPPING	TRUE

Table 29: Feature Flags for dynamic mapping configuration



### 4.1.6.3 Configuration sequence

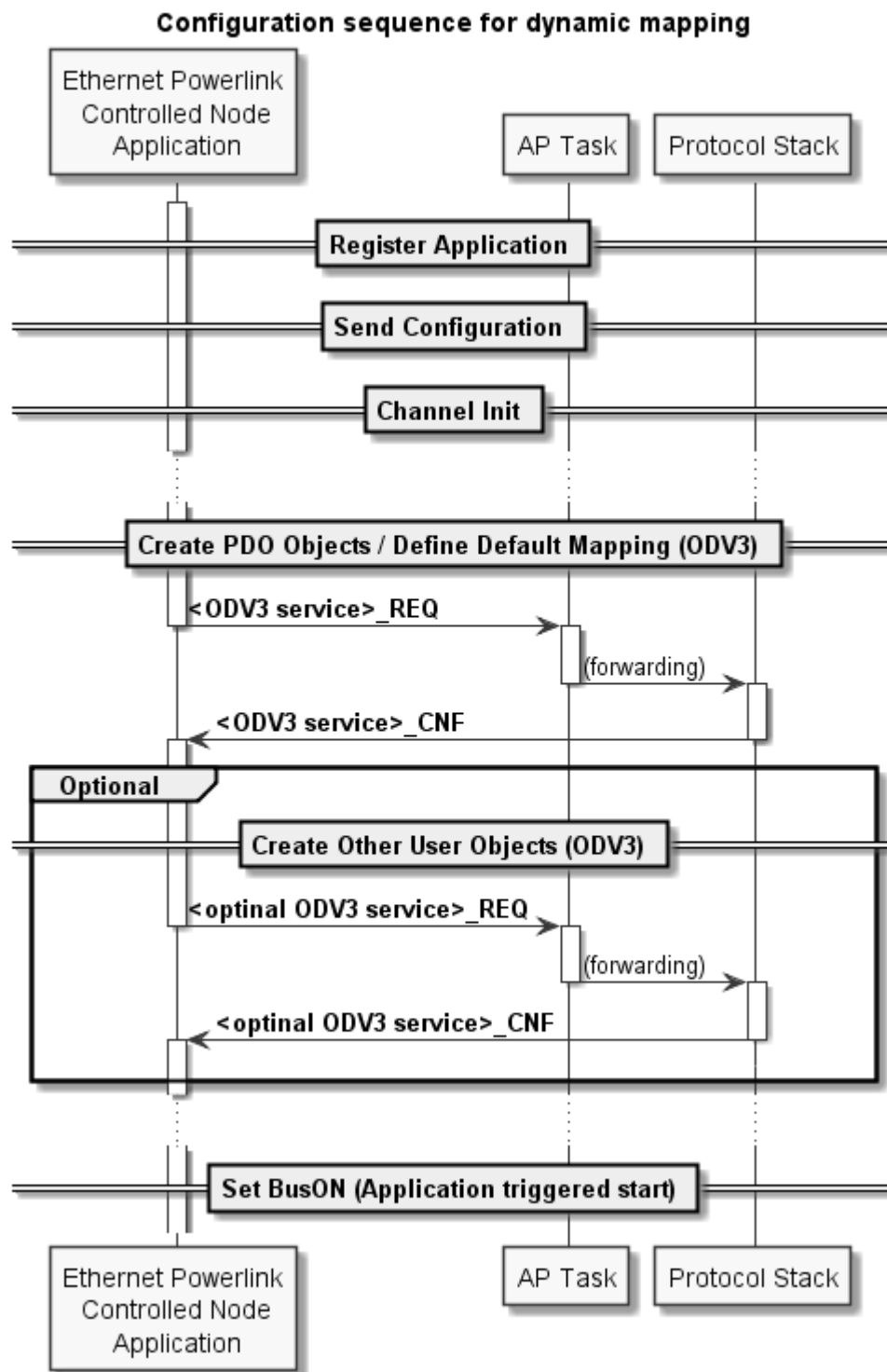


Figure 8: Sequence of dynamic mapping configuration

#### 4.1.6.4 Set PDO size service

This service has to be used in dynamic PDO mapping devices to set the new PDO size and mapping version. This is needed after a new mapping configuration is received from the bus and is done in following steps:

- The application receives ODV3 write indications for the objects 0x1400, 0x1600, 0x1800 and 0x1A00. If the cross traffic feature is used, then write indications will also be received for the objects 0x1401...0x140N and 0x1601...0x160N.
- The application calculates the new PDO size
- The application sets the new PDO size and mapping version to the stack
- The new settings are now stored in the stack, but are only made active after a configuration reset command received from the bus

See next section for detailed sequence diagram of this service.

## Packet structure reference

```
typedef struct EPLCN_IF_SET_PDO_SIZE_REQ_DATA_Ttag
{
    /** Poll Request data size (range 0 to 1490) */
    TLR_UINT16      usPReqDataSize;
    /** Poll Response data size (range 0 to 1490) */
    TLR_UINT16      usPResDataSize;
    /** PReq Mapping Version */
    TLR_UINT8       bPReqMappingVersion;
    /** PRes Mapping Version */
    TLR_UINT8       bPResMappingVersion;
} EPLCN_IF_SET_PDO_SIZE_REQ_DATA_T;

typedef struct EPLCN_IF_SET_PDO_SIZE_REQ_Ttag
{
    TLR_PACKET_HEADER_T      tHead;
    EPLCN_IF_SET_PDO_SIZE_REQ_DATA_T  tData;
} EPLCN_IF_SET_PDO_SIZE_REQ_T;
```

## Packet description

Structure EPLCN_IF_SET_PDO_SIZE_REQ_T			Type: Request
Variable	Type	Value / Range	Description
<b>tHead - Structure</b> TLR_PACKET_HEADER_T			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	6	Packet Data Length in bytes
ulId	UINT32	0 ... 2 <sup>32</sup> -1	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA232	EPLCN_IF_SET_PDO_SIZE_REQ - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch
<b>tData - Structure</b> EPLCN_IF_SET_PDO_SIZE_REQ_DATA_T			
usPReqDataSize	UINT16	0..1490	New Poll Request data size (Receive data).
usPResDataSize	UINT16	0..1490	New Poll Response data size (Transmit data).
bPReqMappingVersion	UINT8	0..255	Mapping version of the new Poll Request mapping configuration.
bPResMappingVersion	UINT8	0..255	Mapping version of the new Poll Response mapping configuration.

Table 30: EPLCN\_IF\_SET\_PDO\_SIZE\_REQ – Set PDO Size request

## Packet structure reference

```
typedef struct EPLCN_IF_SET_PDO_SIZE_CNF_Ttag
{
    TLR_PACKET_HEADER_T          tHead;
} EPLCN_IF_SET_PDO_SIZE_CNF_T;
```

## Packet description

Structure EPLCN_IF_SET_PDO_SIZE_CNF_T			Type: Confirmation
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	0	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA233	EPLCN_IF_SET_PDO_SIZE_CNF - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch

Table 31: EPLCN\_IF\_SET\_PDO\_SIZE\_CNF – Set PDO Size confirmation

#### 4.1.6.5 Set PDO Size sequence

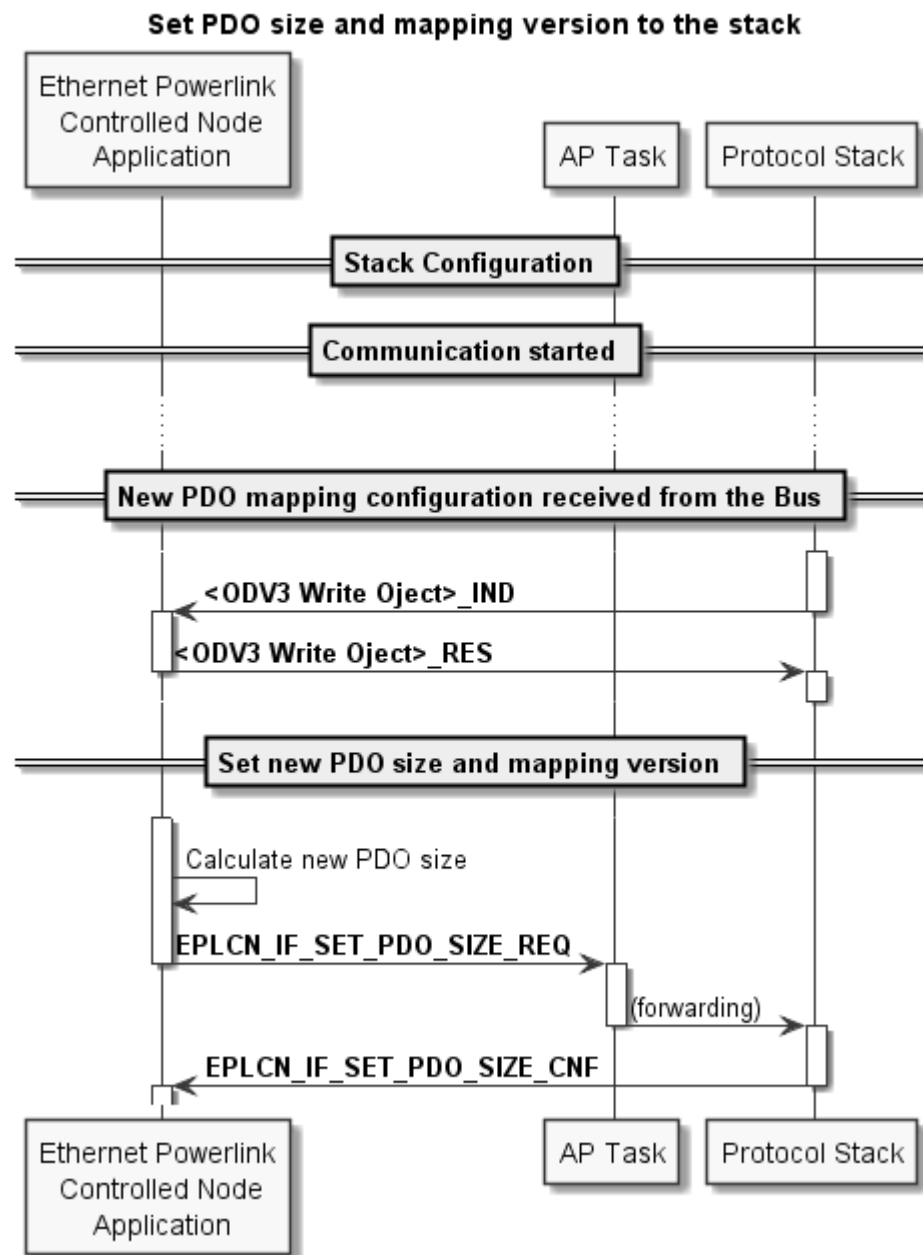


Figure 9: Sequence of Set PDO Size service

## 4.1.7 Optional Services

This chapter describes the optional services provided by the stack.

### 4.1.7.1 Configure NodeId

This service allows the application to reconfigure the NodeId without sending a new Set\_Configuration packet. The value of the new NodeId will be activated after the device goes through the state NMT\_GS\_RESET\_CONFIGURATION again. This is triggered by following events:

- NMT reset commands
- Channellnit request packet
- Setting BusOff and BusOn with the StartStopCommunication packet.

#### Use-case example

Device provides DIP-Switch to configure the NodeId. After the Controlled Node was configured and is running, the user may set a new NodeId over the switch. In this case using this service allows setting the new NodeId to stack without going through the complete configuration process again.

## Packet structure reference

```
typedef struct EPLCN_IF_SET_NODE_ID_REQ_DATA_Ttag
{
    TLR_UINT8    bNodeId;
} EPLCN_IF_SET_NODE_ID_REQ_DATA_T;

typedef struct EPLCN_IF_SET_NODE_ID_REQ_Ttag
{
    TLR_PACKET_HEADER_T    tHead;
    EPLCN_IF_SET_NODE_ID_REQ_DATA_T    tData;
} EPLCN_IF_SET_NODE_ID_REQ_T;
```

## Packet description

Structure EPLCN_IF_SET_NODE_ID_REQ_T			Type: Request
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	1	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA234	EPLCN_IF_SET_NODE_ID_REQ - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch
<b>tData - Structure EPLCN_IF_SET_PDO_SIZE_REQ_DATA_T</b>			
bNodeId	UINT8	1..239	New NodeId value. This value will be activate while in NMT_GS_RESET_CONFIGURATION

Table 32: EPLCN\_IF\_SET\_NODE\_ID\_REQ – Set NodeId request

## Packet structure reference

```
typedef struct EPLCN_IF_SET_NODE_ID_CNF_Ttag
{
    TLR_PACKET_HEADER_T          tHead;
} EPLCN_IF_SET_NODE_ID_CNF_T;
```

## Packet description

Structure EPLCN_IF_SET_NODE_ID_CNF_T			Type: Confirmation
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	0	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA235	EPLCN_IF_SET_NODE_ID_CNF - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch

Table 33: EPLCN\_IF\_SET\_NODE\_ID\_CNF – Set NodeId confirmation



## 4.2 NMT State control

### 4.2.1 Architecture of NMT State control

As described in section *State machine (NMT)* (page 16), following NMT state machine transitions have to be controlled by the application if the corresponding configuration flag is set (see section *Stack Configuration Flags* on page 45

MSK\_EPLCN\_IF\_CFG\_STACK\_CFG\_FLAGS\_NMT\_TRIGGERED\_BY\_APP) :

Source NMT State	Target NMT State
NMT_GS_INITIALISING	NMT_GS_RESET_APPLICATION
NMT_GS_RESET_APPLICATION	NMT_GS_RESET_COMMUNICATION
NMT_GS_RESET_COMMUNICATION	NMT_GS_RESET_CONFIGURATION
NMT_GS_RESET_CONFIGURATION	NMT_CS_NOT_ACTIVE
NMT_CS_PRE_OPERATIONAL_2	NMT_CS_READY_TO_OPERATE

Table 34: NMT state transitions controlled by the application

The Controlled Node implements a separation between target state setting and changing the NMT state. The packets for the setting of the target state will only set the new NMT state to reach. The confirmations return immediately. So, these do not indicate completion of the NMT state change at all.

In order to determine successful state change and current NMT state, the following service has to be used:

- Current NMT State indication (page 82)

#### Error Codes related to successful operation

- TLR\_S\_OK (0x00000000)  
The Controlled Node has accepted the new target state and will proceed to it.
- ERR\_EPLCN\_NMT\_INVALID\_STATE\_CHANGE (0xC0E60001)  
This error may happen when the application triggers a state change which is exclusively triggered by the bus (i.e. Change from NMT\_CS\_NOT\_PRE\_OPERATIONAL\_1 to NMT\_CS\_NOT\_PRE\_OPERATIONAL\_2) or an invalid state change (i.e. from NMT\_GS\_INITIALISING directly to NMT\_GS\_RESET\_CONFIGURATION).

#### Indications and setting the Target NMT State

When the NMT state change indication indicates the same state as the last issued Set Target NMT State request, the state change has been completed successfully.

#### Handling overview

The sequences in section *Set State sequence* (page 76) provide examples of the handling to be done for the NMT state control.

## 4.2.2 Services

### 4.2.2.1 Set NMT State service

The service is used for requesting a target NMT state specified in `bTargetState`.

If the packet has been returned with `ulSta` equal to `TLR_S_OK`, the Controlled Node will change its NMT state to the newly requested target state.

In order to determine successful state change and current NMT state, the service *Current NMT State indication* (page 82) has to be used.

#### Packet structure reference

```
typedef struct EPLCN_IF_NMT_SET_STATE_REQ_DATA_Ttag
{
    TLR_UINT8    bTargetState;
} EPLCN_IF_NMT_SET_STATE_REQ_DATA_T;

typedef struct EPLCN_IF_NMT_SET_STATE_REQ_Ttag
{
    TLR_PACKET_HEADER_T    tHead;
    EPLCN_IF_NMT_SET_STATE_REQ_DATA_T    tData;
} EPLCN_IF_NMT_SET_STATE_REQ_T;
```

#### Packet description

Structure EPLCN_IF_NMT_SET_STATE_REQ_T			Type: Request
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	1	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA202	EPLCN_IF_NMT_SET_STATE_REQ - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch
<b>tData - Structure EPLCN_IF_NMT_SET_STATE_REQ_DATA_T</b>			
bTargetState	UINT8		Target NMT State. Refer to section <i>State machine (NMT)</i> (page 16) for information about the allowed state changes. Refer to section <i>Values for identifying NMT states in packets</i> (page 37) for information about coding the NMT state in the Packets

Table 35: EPLCN\_IF\_NMT\_SET\_STATE\_REQ – Set NMT State request

## Packet structure reference

```
typedef struct  EPLCN_IF_NMT_SET_STATE_CNF_Ttag
{
    TLR_PACKET_HEADER_T          tHead;
} EPLCN_IF_NMT_SET_STATE_CNF_T;
```

## Packet description

Structure EPLCN_IF_NMT_SET_STATE_CNF_T			Type: Confirmation
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	0	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA203	EPLCN_IF_NMT_SET_STATE_CNF - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch

Table 36: EPLCN\_IF\_NMT\_SET\_STATE\_CNF – Set NMT State confirmation

### 4.2.3 Set State sequence

This section provides two sequence examples for the NMT state control handling.

#### 4.2.3.1 Successful state change

The following sequence describes the NMT state control handling for successfully changing to next state by using the indication described in section *Current NMT State indication* (page 82) and the request described in section *Set NMT State service* (page 74).

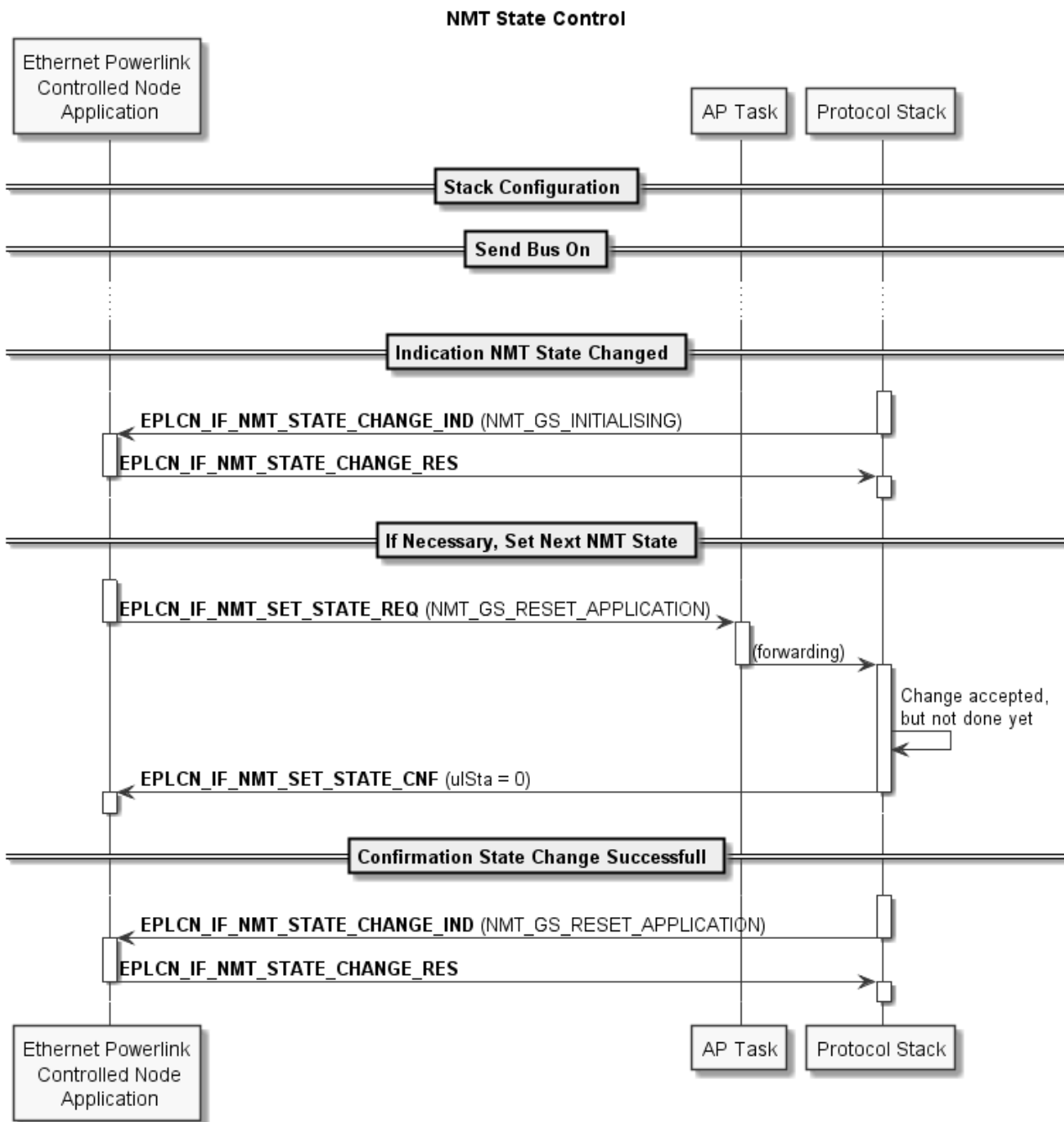


Figure 10: Example of successful NMT state change

### 4.2.3.2 Erroneous state change

The following sequence shows two examples of an erroneous usage of the NMT state control service described in section *Set NMT State service* on page 74. In the first example, the application tries to skip the state NMT\_GS\_RESET\_APPLICATION, and set the next state. The second example shows the handling in case of trying to set an NMT state which shall not be triggered by the application.

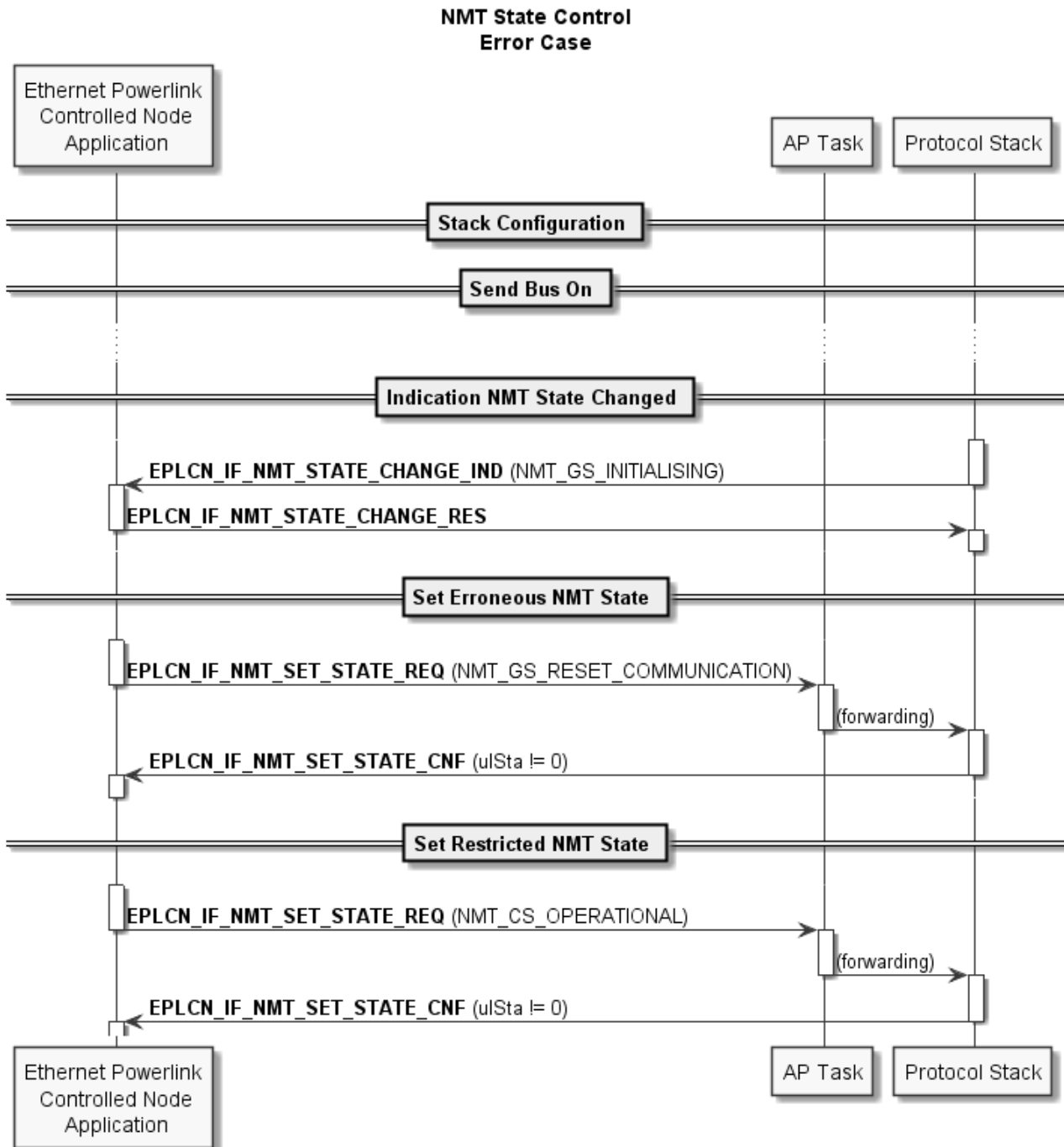


Figure 11: Example of erroneous NMT state change

## 4.3 Status indications

### 4.3.1 Registration and deregistration of Status Indications

#### 4.3.1.1 Register for Status Indications service

This packet registers an application task for receiving status indications.

The following groups of status indications will be sent to the application task after successful registration:

If the application does not want to receive those indications anymore, it has to use the service described in subsection *Unregister from Status Indications service* on page 80.

#### Packet structure reference

```
typedef struct RCX_REGISTER_APP_REQ_Ttag
{
    TLR_PACKET_HEADER_T tHead;
} RCX_REGISTER_APP_REQ_T;
```

#### Packet description

Structure RCX_REGISTER_APP_REQ_T			Type: Request
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	0	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0x2F10	RCX_REGISTER_APP_REQ - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch

Table 37: RCX\_REGISTER\_APP\_REQ – Register for Status Indications request

## Packet structure reference

```
typedef struct RCX_REGISTER_APP_CNF_Ttag
{
    TLR_PACKET_HEADER_T    tHead;
} RCX_REGISTER_APP_CNF_T;
```

## Packet description

Structure RCX_REGISTER_APP_CNF_T			Type: Confirmation
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination queue handle, unchanged
ulSrc	UINT32		Source queue handle, unchanged
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	0	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification, unchanged
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0x2F11	RCX_REGISTER_APP_CNF – Command
ulExt	UINT32	0	Extension, reserved
ulRout	UINT32	x	Routing information, do not change

Table 38: RCX\_REGISTER\_APP\_CNF – Register for Status Indications confirmation

### 4.3.1.2 Unregister from Status Indications service

This packet deregisters an application task from receiving status indications.

The following status indications will not continue to be sent to the application task anymore after successful deregistration.

#### Packet structure reference

```
typedef struct RCX_UNREGISTER_APP_REQ_Ttag
{
    TLR_PACKET_HEADER_T tHead;
} RCX_UNREGISTER_APP_REQ_T;
```

#### Packet description

Structure RCX_UNREGISTER_APP_REQ_T			Type: Request
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	0	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0x2F12	RCX_UNREGISTER_APP_REQ - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch

Table 39: RCX\_UNREGISTER\_APP\_REQ – Unregister for Status Indications request



## Packet structure reference

```
typedef struct RCX_UNREGISTER_APP_CNF_Ttag
{
    TLR_PACKET_HEADER_T    tHead;
} RCX_UNREGISTER_APP_CNF_T;
```

## Packet description

Structure RCX_UNREGISTER_APP_CNF_T			Type: Confirmation
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination queue handle, unchanged
ulSrc	UINT32		Source queue handle, unchanged
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	0	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification, unchanged
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0x2F13	RCX_UNREGISTER_APP_CNF – Command
ulExt	UINT32	0	Extension, reserved
ulRout	UINT32	x	Routing information, do not change

Table 40: RCX\_UNREGISTER\_APP\_CNF – Unregister for Status Indications confirmation

## 4.3.2 Common indications

### 4.3.2.1 Current NMT State indication

This packet indicates the new NMT state of the stack. For details on how this indication relates to NMT state control, see section *NMT State control* (page 73).

This indication contains also information about the stack indicators (Status and Error LEDs). The application can derive the Status LED state from the current NMT state. The Error LED is indicated explicitly in `fErrorLedIsOn`. More information about indicators handling for Ethernet POWERLINK can be found in section *LED Status* (page 109).

#### Packet structure reference

```
typedef struct EPLCN_IF_NMT_STATE_CHANGE_IND_DATA_Ttag
{
    TLR_UINT8    bCurrentState;
    TLR_UINT8    fErrorLedIsOn;
} EPLCN_IF_NMT_STATE_CHANGE_IND_DATA_T;

typedef struct EPLCN_IF_NMT_STATE_CHANGE_IND_Ttag
{
    TLR_PACKET_HEADER_T    tHead;
    EPLCN_IF_NMT_STATE_CHANGE_IND_DATA_T    tData;
} EPLCN_IF_NMT_STATE_CHANGE_IND_T;
```

#### Packet description

Structure EPLCN_IF_NMT_STATE_CHANGE_IND_T			Type: Indication
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	2	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA200	EPLCN_IF_NMT_STATE_CHANGE_IND - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch
<b>tData - Structure EPLCN_IF_NMT_STATE_CHANGE_IND_DATA_T</b>			
bCurrentState	UINT8		Current NMT State indicated. Refer to section <i>Values for identifying NMT states in packets</i> (page 37) for information about coding of the NMT states
fErrorLedIsOn	UINT8	0,1	Current Error LED state. 0 = Error LED Off 1 = Error LED On

Table 41: EPLCN\_IF\_NMT\_STATE\_CHANGE\_IND – Current NMT State indication

## Packet structure reference

```
typedef struct  EPLCN_IF_NMT_STATE_CHANGE_RES_Ttag
{
    TLR_PACKET_HEADER_T          tHead;
} EPLCN_IF_NMT_STATE_CHANGE_RES_T;
```

## Packet description

Structure EPLCN_IF_NMT_STATE_CHANGE_RES_T			Type: Response
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	0	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA201	EPLCN_IF_NMT_STATE_CHANGE_RES - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch

Table 42: EPLCN\_IF\_NMT\_STATE\_CHANGE\_RES – Current NMT State response

### 4.3.2.2 Enable Ready To Operate Command indication

This packet indicates the reception of the NMT command EnableReadyToOperate from the bus and indicates that the MN requests the change of state to NMT\_CS\_READY\_TO\_OPERATE. See section *State machine (NMT)* (page 16).

If the NMT state machine is triggered by the application (see section *Stack Configuration Flags* page 45), this command has to be confirmed by set NMT state to NMT\_CS\_READY\_TO\_OPERATE.

#### Packet structure reference

```
typedef struct  EPLCN_IF_NMT_CMD_ENABLE_RDY_TO_OPERATE_IND_Ttag
{
    TLR_PACKET_HEADER_T          tHead;
} EPLCN_IF_NMT_CMD_ENABLE_RDY_TO_OPERATE_IND_T;
```

#### Packet description

Structure EPLCN_IF_NMT_CMD_ENABLE_RDY_TO_OPERATE_IND_T			Type: Indication
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	0	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA210	EPLCN_IF_NMT_CMD_ENABLE_RDY_TO_OPERATE_IND - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch

Table 43: EPLCN\_IF\_NMT\_CMD\_ENABLE\_RDY\_TO\_OPERATE\_IND – NMT Command EnableReadyToOperate indication

## Packet structure reference

```
typedef struct  EPLCN_IF_NMT_CMD_ENABLE_RDY_TO_OPERATE_RES_Ttag
{
    TLR_PACKET_HEADER_T          tHead;
} EPLCN_IF_NMT_CMD_ENABLE_RDY_TO_OPERATE_RES_T;
```

## Packet description

Structure EPLCN_IF_NMT_CMD_ENABLE_RDY_TO_OPERATE_RES_T			Type: Response
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	0	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA211	EPLCN_IF_NMT_CMD_ENABLE_RDY_TO_OPERATE_RES - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch

*Table 44: EPLCN\_IF\_NMT\_CMD\_ENABLE\_RDY\_TO\_OPERATE\_RES – NMT Command EnableReadyToOperate response*

### 4.3.3 Indications related to cross traffic

#### 4.3.3.1 Cross traffic configured indication

The indication packet `EPLCN_IF_CROSS_TRAFFIC_CONFIGURED_IND/RES` provides information about how to retrieve the PRes data of the different listening NodeIDs.

These include

- the offset within the dual-port memory, where the stack will copy the PRes data of the different listening NodeIDs (Parameter `usDpmOffset`)
- the size of the area containing these data (Parameter `usDataSize`)
- the NodeID itself (Parameter `bNodeId`) .

#### Packet structure reference

```

/*****
/* Packet: EPLCN_IF_CROSS_TRAFFIC_CONFIGURED_IND */
/*****
#define EPLCN_IF_CROSS_TRAFFIC_CFG_OFFSET_INVALID    0xFFFF
#define EPLCN_IF_CROSS_TRAFFIC_MAX_SUPPORTED_NODES    8
typedef struct EPLCN_IF_CROSS_TRAFFIC_CFG_DATA_Ttag
{
    uint8_t    bNodeId;
    uint8_t    abReserved[3];
    uint16_t    usDataSize;
    uint16_t    usDpmOffset; /* if the node PRes could not be mapped, the offset will be set
to EPLCN_IF_CROSS_TRAFFIC_CFG_OFFSET_INVALID.
                        * However, in this case the value of usDataSize will still
showing the real PRes length*/
} EPLCN_IF_CROSS_TRAFFIC_CFG_DATA_T;

typedef struct EPLCN_IF_CROSS_TRAFFIC_CONFIGURED_IND_DATA_Ttag
{
    EPLCN_IF_CROSS_TRAFFIC_CFG_DATA_T atConfig[EPLCN_IF_CROSS_TRAFFIC_MAX_SUPPORTED_NODES];
} EPLCN_IF_CROSS_TRAFFIC_CONFIGURED_IND_DATA_T;

typedef struct EPLCN_IF_CROSS_TRAFFIC_CONFIGURED_IND_Ttag
{
    HIL_PACKET_HEADER_T            tHead;
    EPLCN_IF_CROSS_TRAFFIC_CONFIGURED_IND_DATA_T    tData;
} EPLCN_IF_CROSS_TRAFFIC_CONFIGURED_IND_T;
/*****/

```

**Packet description**

Structure EPLCN_IF_CROSS_TRAFFIC_CONFIGURED_IND_T			Type: Indication
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	8	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA236	EPLCN_IF_CROSS_TRAFFIC_CONFIGURED_IND - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch
<b>tData - Structure EPLCN_IF_CROSS_TRAFFIC_CONFIGURED_IND_DATA_T</b>			
bNodeId	UINT8	1.. 254	Id of the node from which the PRes data originates
abReserved[3]	UINT8		Reserved field for alignment purposes
usDataSize	UINT16		Data size of the PRes data
usDpmOffset	UINT16		Offset where to find the PRes data

Table 45: EPLCN\_IF\_CROSS\_TRAFFIC\_CONFIGURED\_IND – Cross traffic configured indication

## Packet structure reference

```

/*****
/* Packet: EPLCN_IF_CROSS_TRAFFIC_CONFIGURED_RES */
/*****

typedef struct EPLCN_IF_CROSS_TRAFFIC_CONFIGURED_RES_Ttag
{
    HIL_PACKET_HEADER_T          tHead;
} EPLCN_IF_CROSS_TRAFFIC_CONFIGURED_RES_T;

/*****

```

## Packet description

Structure EPLCN_IF_CROSS_TRAFFIC_CONFIGURED_RES_T			Type: Response
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination queue handle, unchanged
ulSrc	UINT32		Source queue handle, unchanged
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	0	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification, unchanged
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA237	EPLCN_IF_CROSS_TRAFFIC_CONFIGURED_RES - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch

Table 46: EPLCN\_IF\_CROSS\_TRAFFIC\_CONFIGURED\_RES – Cross traffic configured response



## 4.4 Diagnosis

This section describes the diagnosis mechanisms of Ethernet POWERLINK Controlled Node protocol and the corresponding API provided by the stack.

A detailed description of the diagnosis signaling is described in reference 3 (Ethernet Powerlink Communication Profile Specification; EPSG DS 301 V1.4.0; 20, section 6.5)

An EPL Controlled Node may provide both communication profile (ref. 3, App. 3.9) and vendor or device profile specific diagnosis information.

The communication profile diagnosis (such as loss of frames errors) are detected and handled directly by the stack.

Vendor or device profile specific diagnosis has to be handled by the user application using the corresponding API functionalities.

Detected diagnosis and errors are described using one or more of the following information blocks:

- Static Error Bit Field
- Error Entry
- Status Entry

The Static Error Bit Field and Status Entry information are automatically set by the stack into the Status Response frame. The Error Entry information is always set into the Error History object (0x1003) of the Object Dictionary and, depending on the configuration of each entry, may be also included in the Status Response frame.

### 4.4.1 Error detection

This section explains how the different errors / diagnosis may be detected.

#### 4.4.1.1 Communication profile errors

The communication profile errors are detected and handled directly by the stack.

#### Errors detected by the Data Link Layer (DLL)

In this lower layer of the firmware, the hardware and bus frames errors are detected. The errors detected here are:

- Loss of frames (SoC, SoA, PReq)
- Frame collision
- CRC errors in received frames
- SoC Jitter out of range

These errors may not be signaled each time they are detected, since they use a threshold mechanism. Every time one of these errors is detected a corresponding counter is incremented by 8. When this counter reaches the configured Threshold, the specific communication error is signaled using an Error Entry.

Every elapsed cycle without detection of the error, the counter is decremented by 1.

---

**Note:** The user application may configure the error Thresholds level and also deactivated some all of the error detections but the Loss of SoC.

---

### Errors detected by the PDO unit (IF task)

These errors are related to the PDO data validity while in Operational mode. The errors detected by the Hilscher's here are:

- Received PDO are too short
- Mapping version of the received PDO is incorrect

Every time one of these errors is detected a specific communication error is signaled within an Error Entry.

#### 4.4.1.2 Vendor and device profile specific errors / diagnosis

These diagnoses have to be detected and handled by the application using the corresponding API.

### 4.4.2 Error signaling mechanism

This section contains an overview of the error signaling mechanism provided by the EPL protocol.

#### 4.4.2.1 Static Error Bit Field

The static error bit field is an 8 bytes data block containing general diagnosis information (without specific error codes). This area is included in the Status Response frame.

The following table shows the structure of this diagnosis information:

Byte	Description
6	Contents of the Error Register in OD (Object 0x1001)
7	Reserved
8-13	Errors specific to device profile or vendor

Table 47: Static Error Bit Field

Refer to Write Static Error Bit Field on page 93 for detailed information about the API for the static error bit field.

#### 4.4.2.2 Error Entry

The error entry is used to signal that an error was detected. This entry may contain a communication profile, a vendor specific or a device profile error. All errors signaled with the error entry are set into the Error History object (0x1003). This is done by the stack automatically. Additionally, the error may be written into the Emergency Queue of the Status Response frame.

The structure of the Status Entry is explained in the section *Error / Status Entry format* on page 91.

Refer to section *Write Status Entry service* on page 99 for detailed information about the API for the error entries.

### 4.4.2.3 Status Entry

The status entry is a vendor specific entry to be written by the application. The stack sends this diagnosis data within the Status Response frame.

The structure of the Status Entry is explained in the section Error / Status Entry format on page 91.

Refer to section *Write Status Entry service* on page 99 for detailed information about the API for the status entries.

### 4.4.2.4 Error / Status Entry format

The following table shows the structure of an error entry (also used as status entry):

Byte No.	Field	Type
0-1	Entry type	UNSIGNED16
2-3	Error code	UNSIGNED16
4-11	Time stamp	UNSIGNED64
12-19	Additional information	UNSIGNED64

Table 48: Structure of an Error / Status Entry

#### Entry type

Entry type		
Bit	Value	Description
D15	<b>Determines whether it is an Error Entry or a Status Entry</b>	
	0	Error Entry. The entry will be set in the Error History automatically by the stack
	1	Status Entry. The entry will be set in the corresponding position in the Status Response frame
D14	<b>This is only checked if it is an Error Entry (D15=0) and determines if the entry should also set in the Status Response frame</b>	
	0	No. Error Entry appears only in the Error History
	1	Yes. Additionally to the Error History, the Error Entry appears also in the Status Response frame
D13 - D12	<b>Mode</b>	
	0	Entry is invalid. This is only internally by the stack for the error signaling mechanism. The user application is not allowed to write an Error or Status Entry with this mode to the stack.
	1	An error occurred and is still active.
	2	An active error was cleared. (Only for Error Entries)
	3	An error occurred (Only for Error Entries)

Entry type		
Bit	Value	Description
<b>D11 – D0</b>	<b>Profile. This determines how to decode the value of Error Code field.</b>	
	0x000	Reserved – do not use!
	0x001	Error Code indicates a vendor-specific code
	0x002	Error Code indicates a communication profile specific error. The possible values are defined in the Ethernet Powerlink specification (reference 3, App. 3.9 “Error Code Constants”).
	0x003 - 0xFFFF	Error Code indicates a device profile specific error. The value depends from device profile.

Table 49: Entry type

## Error code

This is the error code of the entry. The following table shows the supported and detectable communication errors (Profile = 1 in Entry Type) in the Hilscher’s EPL Controlled Node stack.

Category: Hardware errors (0x816n)	
Name	Value
E_DLL_COLLISION_TH	0x8163
E_DLL_CRC_TH	0x8164

Table 50: Error Entries within Status Response Frames - Hardware errors

Category: Frame size errors (0x821n)	
Name	Value
E_PDO_SHORT_RX	0x8210
E_PDO_MAP_VERS	0x8211

Table 51: Error Entries within Status Response Frames - Frame size errors

Category: Frame errors (0x824n)	
Name	Value
E_DLL_JITTER_TH	0x8235
E_DLL_LOSS_PREQ_TH	0x8242
E_DLL_LOSS_SOA_TH	0x8244
E_DLL_LOSS_SOC_TH	0x8245

Table 52: Error Entries within Status Response Frames – Loss of frame errors

## Time Stamp

The time stamp contains the current SoC net time at exactly the cycle where the event occurred/ the error was detected.

---

**Note:** The time stamp for both Status Entries and Error Entries is automatically set in the stack. The application does not need this field.

---

## Additional Info

This area may contain vendor-specific or device profile specific information. For communication profile errors, there is no additional info field.

### 4.4.3 Write Diagnosis to Stack

This section describes the services provided by the EPL Controlled Node stack to write a diagnosis.

#### 4.4.3.1 Write Static Error Bit Field service

This service has to be used to write one or more bits of the Static Error Bit Field into the Status Response frame.

A successful confirmation means that the new bits' values are stored in non-volatile memory and will be included in the next update of the Status Response frame, when requested by the MN.

#### Error codes related to successful operation

- TLR\_S\_OK (0x00000000)  
New values are stored internally and waiting for next update of Status Response frame.
- ERR\_EPLCN\_NMT\_INVALID\_STATIC\_FIELD\_BIT\_NUMBER (0xC0E60009)  
Specified bit number is invalid. Invalid Bits are:
  - Bit  $\geq 64$ : Bit is outer field border. Static Error Bit Field has a length of 8 Bytes.
  - $8 \leq \text{Bit} \leq 15$ : These bits are reserved.

## Packet structure reference

```
typedef struct EPLCN_IF_NMT_WRITE_STATIC_ERROR_BIT_FIELD_REQ_DATA_ENTRY_Ttag
{
    TLR_UINT8                bBitNumber;
    TLR_BOOLEAN8             fBitValue;
} EPLCN_IF_NMT_WRITE_STATIC_ERROR_BIT_FIELD_REQ_DATA_ENTRY_T;

typedef struct EPLCN_IF_NMT_WRITE_STATIC_ERROR_BIT_FIELD_REQ_DATA_Ttag
{
    EPLCN_IF_NMT_WRITE_STATIC_ERROR_BIT_FIELD_REQ_DATA_ENTRY_T  atEntry[1];
} EPLCN_IF_NMT_WRITE_STATIC_ERROR_BIT_FIELD_REQ_DATA_T;

typedef struct EPLCN_IF_NMT_WRITE_STATIC_ERROR_BIT_FIELD_REQ_Ttag
{
    TLR_PACKET_HEADER_T                tHead;
    EPLCN_IF_NMT_WRITE_STATIC_ERROR_BIT_FIELD_REQ_DATA_T  tData;
} EPLCN_IF_NMT_WRITE_STATIC_ERROR_BIT_FIELD_REQ_T;
```

## Packet description

Structure EPLCN_IF_NMT_WRITE_STATIC_ERROR_BIT_FIELD_REQ_T			Type: Request
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	N * 2	2 = length of one entry N = Number of bits to be specified in the packet
ulId	UINT32	0 ... 2 <sup>32</sup> -1	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA228	EPLCN_IF_NMT_WRITE_STATIC_ERROR_BIT_FIELD_REQ - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch
<b>tData - Structure EPLCN_IF_NMT_WRITE_STATIC_ERROR_BIT_FIELD_REQ_DATA_T</b>			
atEntry[1]	Struct		Entry for one Bit. This entry specifies the bit number and its new value. To specify more than one bit, just write in the next indexes of atEntry. The number of entries is specified with the parameter ulLen.

Table 53: EPLCN\_IF\_NMT\_WRITE\_STATIC\_ERROR\_BIT\_FIELD\_REQ – Write Static Error Bit Field request

## Packet structure reference

```
typedef struct EPLCN_IF_NMT_WRITE_STATIC_ERROR_BIT_FIELD_CNF_Ttag
{
    TLR_PACKET_HEADER_T                                tHead;
} EPLCN_IF_NMT_WRITE_STATIC_ERROR_BIT_FIELD_CNF_T;
```

## Packet description

Structure EPLCN_IF_NMT_WRITE_STATIC_ERROR_BIT_FIELD_CNF_T			Type: Confirmation
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	0	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA229	EPLCN_IF_NMT_WRITE_STATIC_ERROR_BIT_FIELD_CNF - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch

Table 54: EPLCN\_IF\_NMT\_WRITE\_STATIC\_ERROR\_BIT\_FIELD\_CNF – Write Static Error Bit Field confirmation

#### 4.4.3.2 Write Error Entry service

This packet has to be used to write an Error Entry to the stack. This will be automatically written in the Error History object and, depending on the entry type, will also send within the Status Response frame.

A successful confirmation means that the entry is already set in the Error History. The send of the diagnosis in within Status Response may not happen yet, since this is send only when requested by the MN.

##### Error Codes related to successful operation

- TLR\_S\_OK (0x00000000)  
The Controlled Node has processed the entry successfully.
- ERR\_EPLCN\_NMT\_ENTRY\_TYPE\_IS\_NOT\_ERROR\_ENTRY (0xC0E60005)  
Bit D15 of the Entry Type is not 0.



## Packet structure reference

```
typedef struct EPLCN_IF_NMT_WRITE_ERROR_ENTRY_REQ_DATA_Ttag
{
    TLR_UINT16      usEntryType;
    TLR_UINT16      usErrorCode;
    TLR_UINT8       abAddInformation[8];
} EPLCN_IF_NMT_WRITE_ERROR_ENTRY_REQ_DATA_T;

typedef struct EPLCN_IF_NMT_WRITE_ERROR_ENTRY_REQ_Ttag
{
    TLR_PACKET_HEADER_T      tHead;
    EPLCN_IF_NMT_WRITE_ERROR_ENTRY_REQ_DATA_T tData;
} EPLCN_IF_NMT_WRITE_ERROR_ENTRY_REQ_T;
```

## Packet description

Structure EPLCN_IF_NMT_WRITE_ERROR_ENTRY_REQ_T			Type: Request
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	12	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA220	EPLCN_IF_NMT_WRITE_ERROR_ENTRY_REQ - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch
<b>tData - Structure EPLCN_IF_NMT_WRITE_ERROR_ENTRY_REQ_DATA_T</b>			
usEntryType	UINT16		Entry Type. Refer to section 4.4.2.4 Error / Status Entry format for information.
usErrorCode	UINT16		Error Code Refer to section 4.4.2.4 Error / Status Entry format for information.
abAddInformation [8]	UINT8[]		Additional Information Refer to section 4.4.2.4 Error / Status Entry format for information.

Table 55: EPLCN\_IF\_NMT\_WRITE\_ERROR\_ENTRY\_REQ – Write Error Entry request

## Packet structure reference

```
typedef struct  EPLCN_IF_NMT_WRITE_ERROR_ENTRY_CNF_Ttag
{
    TLR_PACKET_HEADER_T          tHead;
} EPLCN_IF_NMT_WRITE_ERROR_ENTRY_CNF_T;
```

## Packet description

Structure EPLCN_IF_NMT_WRITE_ERROR_ENTRY_CNF_T			Type: Confirmation
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	0	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA221	EPLCN_IF_NMT_WRITE_ERROR_ENTRY_CNF - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch

Table 56: EPLCN\_IF\_NMT\_WRITE\_ERROR\_ENTRY\_CNF – Write Error Entry confirmation

#### 4.4.3.3 Write Status Entry service

This packet has to be used to write a Status Entry to the stack. This will automatically be written in the Status Response frame in the index specified within the packet and signaled to the MN.

A successful confirmation means that the entry is stored in non-volatile memory and will be included in the next update of the Status Response frame, when requested by the MN.

##### Error codes related to successful operation

- TLR\_S\_OK (0x00000000)  
The Controlled Node has processed the entry successfully.
- ERR\_EPLCN\_NMT\_ENTRY\_TYPE\_IS\_NOT\_STATUS\_ENTRY (0xC0E60006)  
Bit D15 of the Entry Type is not 1.
- ERR\_EPLCN\_NMT\_CONFIGURED\_NUM\_STATUS\_ENTRIES\_EXCEEDED (0xC0E60008)  
The value of usStatusEntryNumber exceeds the configured maximum number of status entries (parameter bNumberOfStatusEntries in the EPLCN\_IF\_SET\_CONFIG\_REQ service).

## Packet structure reference

```
typedef struct EPLCN_IF_NMT_WRITE_STATUS_ENTRY_REQ_DATA_Ttag
{
    TLR_UINT16      usStatusEntryNumber;
    TLR_UINT16      usEntryType;
    TLR_UINT16      usErrorCode;
    TLR_UINT8       abAddInformation[8];
} EPLCN_IF_NMT_WRITE_STATUS_ENTRY_REQ_DATA_T;

typedef struct EPLCN_IF_NMT_WRITE_STATUS_ENTRY_REQ_Ttag
{
    TLR_PACKET_HEADER_T      tHead;
    EPLCN_IF_NMT_WRITE_STATUS_ENTRY_REQ_DATA_T  tData;
} EPLCN_IF_NMT_WRITE_STATUS_ENTRY_REQ_T;
```

## Packet description

Structure EPLCN_IF_NMT_WRITE_STATUS_ENTRY_REQ_T			Type: Request
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	14	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA224	EPLCN_IF_NMT_WRITE_STATUS_ENTRY_REQ - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch
<b>tData - Structure EPLCN_IF_NMT_WRITE_STATUS_ENTRY_REQ_DATA_T</b>			
usStatusEntryNumber	UINT16	0..13	Index in the Status Response frame, where the entry has to appear
usEntryType	UINT16		Entry Type. Refer to section Error / Status Entry format on page 91 for information.
usErrorCode	UINT16		Error Code Refer to section Error / Status Entry format on page 91 for information.
abAddInformation[8]	UINT8[]		Additional Information Refer to section Error / Status Entry format on page 91 for information.

Table 57: EPLCN\_IF\_NMT\_WRITE\_STATUS\_ENTRY\_REQ – Write Status Entry request

## Packet structure reference

```
typedef struct  EPLCN_IF_NMT_WRITE_STATUS_ENTRY_CNF_Ttag
{
    TLR_PACKET_HEADER_T                tHead;
} EPLCN_IF_NMT_WRITE_STATUS_ENTRY_CNF_T;
```

## Packet description

Structure EPLCN_IF_NMT_WRITE_STATUS_ENTRY_CNF_T			Type: Confirmation
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	0	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA225	EPLCN_IF_NMT_WRITE_STATUS_ENTRY_CNF - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch

Table 58: EPLCN\_IF\_NMT\_WRITE\_STATUS\_ENTRY\_CNF – Write Status Entry confirmation

## 4.4.4 Diagnosis indications

This section describes the indications that the application receives from the stack each time a new diagnosis is signaled.

Information about how to register for the stack indications can be found in subsection 4.3.1 “Registration and deregistration of Status Indications”.

### 4.4.4.1 New Error Entry Indication service

This service indicates that a new Error Entry has been set in the stack. In the service packet, the elements Entry Type, Entry Code and Additional Information of the entry are provided. However there is no information about the component which signaled the error.

---

**Note:** The source of the packet is not the component which triggered the error.

---

---

**Note:** An Error Entry set by the application using the Write Error Entry service will also generate an Indication.

---

This service is just an information service. No reaction for the different errors is specified on stack side. The reaction may be application specific.

## Packet structure reference

```
typedef struct EPLCN_IF_NMT_NEW_ERROR_ENTRY_IND_DATA_Ttag
{
    TLR_UINT16      usEntryType;
    TLR_UINT16      usErrorCode;
    TLR_UINT8       abAddInformation[8];
} EPLCN_IF_NMT_NEW_ERROR_ENTRY_IND_DATA_T;

typedef struct EPLCN_IF_NMT_NEW_ERROR_ENTRY_IND_Ttag
{
    TLR_PACKET_HEADER_T      tHead;
    EPLCN_IF_NMT_NEW_ERROR_ENTRY_IND_DATA_T tData;
} EPLCN_IF_NMT_NEW_ERROR_ENTRY_IND_T;
```

## Packet description

Structure EPLCN_IF_NMT_NEW_ERROR_ENTRY_IND_T			Type: Indication
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	12	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA222	EPLCN_IF_NMT_NEW_ERROR_ENTRY_IND - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch
<b>tData - Structure EPLCN_IF_NMT_NEW_ERROR_ENTRY_IND_DATA_T</b>			
usEntryType	UINT16		Entry Type. Refer to section Error / Status Entry format on page 91 for information.
usErrorCode	UINT16		Error Code Refer to section Error / Status Entry format on page 91 for information.
abAddInformation [8]	UINT8[]		Additional Information Refer to section <i>Error / Status Entry format</i> on page 91 for information.

Table 59: EPLCN\_IF\_NMT\_NEW\_ERROR\_ENTRY\_IND – New Error Entry indication

## Packet structure reference

```
typedef struct  EPLCN_IF_NMT_NEW_ERROR_ENTRY_RES_Ttag
{
    TLR_PACKET_HEADER_T          tHead;
} EPLCN_IF_NMT_NEW_ERROR_ENTRY_RES_T;
```

## Packet description

Structure EPLCN_IF_NMT_NEW_ERROR_ENTRY_RES_T			Type: Response
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	0	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA223	EPLCN_IF_NMT_NEW_ERROR_ENTRY_RES - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch

Table 60: EPLCN\_IF\_NMT\_NEW\_ERROR\_ENTRY\_RES – New Error Entry response



#### 4.4.4.2 New Status Entry Indication service

This service indicates that a new Status Entry has been set in the stack. In the service packet, the elements Entry Type, Entry Code and Additional Information of the entry are provided. However there is no information about the component which signaled the Status Entry.

---

**Note:** The source of the packet is not the component which triggered the error.

---

---

**Note:** An Error Entry set by the application using the Write Status Entry service will also generate an Indication.

---

This service is just an information service. No reaction for the different errors is specified on stack side. The reaction may be application specific.

## Packet structure reference

```
typedef struct EPLCN_IF_NMT_NEW_STATUS_ENTRY_IND_DATA_Ttag
{
    TLR_UINT16      usStatusEntryNumber;
    TLR_UINT16      usEntryType;
    TLR_UINT16      usErrorCode;
    TLR_UINT8       abAddInformation[8];
} EPLCN_IF_NMT_NEW_STATUS_ENTRY_IND_DATA_T;

typedef struct EPLCN_IF_NMT_NEW_STATUS_ENTRY_IND_Ttag
{
    TLR_PACKET_HEADER_T      tHead;
    EPLCN_IF_NMT_NEW_STATUS_ENTRY_IND_DATA_T  tData;
} EPLCN_IF_NMT_NEW_STATUS_ENTRY_IND_T;
```

## Packet description

Structure EPLCN_IF_NMT_NEW_STATUS_ENTRY_IND_T			Type: Indication
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	14	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA226	EPLCN_IF_NMT_NEW_STATUS_ENTRY_IND - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch
<b>tData - Structure EPLCN_IF_NMT_NEW_STATUS_ENTRY_IND_DATA_T</b>			
usStatusEntryNumber	UINT16	0..13	Index, where the status entry appears in the Status Response frame on the bus.
usEntryType	UINT16		Entry Type. Refer to section Error / Status Entry format on page 91 for information.
usErrorCode	UINT16		Error Code Refer to section Error / Status Entry format on page 91 for information.
abAddInformation[8]	UINT8[]		Additional Information Refer to section <i>Error / Status Entry format</i> on page 91 for information.

Table 61: EPLCN\_IF\_NMT\_NEW\_STATUS\_ENTRY\_IND – New Status Entry indication

## Packet structure reference

```
typedef struct EPLCN_IF_NMT_NEW_STATUS_ENTRY_RES_Ttag
{
    TLR_PACKET_HEADER_T          tHead;
} EPLCN_IF_NMT_NEW_STATUS_ENTRY_RES_T;
```

## Packet description

Structure EPLCN_IF_NMT_NEW_STATUS_ENTRY_RES_T			Type: Response
Variable	Type	Value / Range	Description
<b>tHead - Structure TLR_PACKET_HEADER_T</b>			
ulDest	UINT32		Destination Queue-Handle
ulSrc	UINT32		Source Queue-Handle
ulDestId	UINT32		Destination End Point Identifier, specifying the final receiver of the packet within the Destination Process. Set to 0 for the Initialization Packet
ulSrcId	UINT32		Source End Point Identifier, specifying the origin of the packet inside the Source Process
ulLen	UINT32	0	Packet Data Length in bytes
ulId	UINT32	0 ... $2^{32}-1$	Packet Identification as unique number generated by the Source Process of the Packet
ulSta	UINT32		See section Status codes / Error codes
ulCmd	UINT32	0xA227	EPLCN_IF_NMT_NEW_STATUS_ENTRY_RES - Command
ulExt	UINT32	0	Extension not in use, set to zero for compatibility reasons
ulRout	UINT32	x	Routing, do not touch

Table 62: EPLCN\_IF\_NMT\_NEW\_STATUS\_ENTRY\_RES – New Status Entry response

### 4.4.5 Extended status block

The POWERLINK Controlled Node stack uses the extended status block for internal diagnosis purposes only. For this reason the content structure is neither distributed nor documented.

## 5 Status codes / Error codes

Hexadecimal Value	Definition Description
0x00000000	TLR_S_OK Status ok
0xC0E60001	ERR_EPLCN_NMT_INVALID_STATE_CHANGE The target state change is not allowed in the current state or because this state change is not to be triggered by the application.
0xC0E60002	ERR_EPLCN_NMT_STACK_NOT_CONFIGURED Channel Init or Bus On are not allowed when the stack is not configured.
0xC0E60003	ERR_EPLCN_NMT_NOT_ALLOWED_IN_ACT_STATE Targeted action not allowed in current NMT state.
0xC0E60004	ERR_EPLCN_NMT_MAX_PDO_SIZE_EXCEEDED Configured PDO exceeds the maximum allowed or maximum configured PDO size.
0xC0E60005	ERR_EPLCN_NMT_ENTRY_TYPE_IS_NOT_ERROR_ENTRY The given entry is not an Error Entry (Bit 15 of Entry Type is not 0).
0xC0E60006	ERR_EPLCN_NMT_ENTRY_TYPE_IS_NOT_STATUS_ENTRY The given entry is not a Status Entry (Bit 15 of Entry Type is not 1).
0xC0E60007	ERR_EPLCN_NMT_MAX_NUM_STATUS_ENTRIES_EXCEEDED Value of parameter bNumberOfStatusEntries in the EPLCN_IF_SET_CONFIG_REQ service is out of the allowed range (0...13).
0xC0E60008	ERR_EPLCN_NMT_CONFIGURED_NUM_STATUS_ENTRIES_EXCEEDED The value of usStatusEntryNumber in EPLCN_IF_WRITE_STATUS_ENTRY_REQ service exceeds the configured maximum number of status entries (parameter bNumberOfStatusEntries in the EPLCN_IF_SET_CONFIG_REQ service).
0xC0E60009	ERR_EPLCN_NMT_INVALID_STATIC_FIELD_BIT_NUMBER Specified bit number is invalid in EPLCN_IF_WRITE_STATIC_ERROR_BIT_FIELD_REQ service is invalid.
0xC0E6000A	ERR_EPLCN_NMT_FSM_AUTO_RUN_ENABLED Stack is running in Auto Run mode (Bit 0 of parameter ulStackCfgFlags in the EPLCN_IF_SET_CONFIG_REQ service is False). State changes cannot be triggered by the application: EPLCN_IF_NMT_SET_STATE_REQ service is not allowed.
0xC0E6000B	ERR_EPLCN_NMT_CONFIGURED_CYCLE_LENGTH_TOO_LOW Value of ulCycleLength in the configuration packet is smaller than the minimum cycle length supported by the device. The minimum cycle length is provided by the parameter ulMinCycleLength of the configuration packet.
0xC0E6000C	ERR_EPLCN_NMT_CONFIGURED_MIN_CYCLE_LENGTH_TOO_LOW Configured value of ulMinCycleLength in the configuration packet is smaller than the current hardware/software combination specific minimums.
0xC0E6000D	ERR_EPLCN_NMT_INVALID_NODE_ID Configured NodeId is invalid.

Table 63: Status codes / Error codes

## 6 Appendix

### 6.1 LED Status

For the POWERLINK Controlled Node protocol, the communication LEDs **BS** (Bus Status) and **BE** (Bus Error) as well as the Ethernet LED **L/A** can assume the status described below. This description is valid from Ethernet POWERLINK protocol (reference 3, chapter 10 "Indicators").














LED	Color	State	Meaning
<b>BS</b> (Bus Status) General name: <b>COM 0</b>	<b>Duo LED red/green</b>		
	 (green)	On	Slave is in ' <b>Operational</b> ' state
	 (green)	Triple Flash	Slave is in ' <b>ReadyToOperate</b> ' state
	 (green)	Double flash	Slave is in ' <b>Pre-Operational 2</b> ' state
	 (green)	Single flash	Slave is in ' <b>Pre-Operational 1</b> ' state
	 (green)	Flickering (10 Hz)	Slave is in ' <b>Basic Ethernet</b> ' state
	 (green)	Blinking (2,5 Hz)	Slave is in ' <b>Stopped</b> ' state
<b>BE</b> (Bus Error) General name: <b>COM 1</b>	 (off)	Off	Slave initializing
	<b>Duo LED red/green</b>		
	 (off)	Off	Slave has no error
<b>L/A</b> Ch0 & Ch1	 (red)	On	Slave has detected an error
	<b>LED green</b>		
	 (green)	On	<b>Link:</b> The device is linked to the Ethernet, but does not send/receive Ethernet frames.
	 (green)	Flickering (load dependent)	<b>Activity:</b> The device is linked to the Ethernet and sends/receives Ethernet frames.
Ch0 & Ch1	 (off)	Off	The device has no link to the Ethernet.
	<b>LED yellow</b>		
	 (off)	Off	This LED is not used.

Table 64: LED states for the POWERLINK Controlled Node

(Continued on next page.)

LED state	Definition
On	The indicator is constantly on.
Off	The indicator is constantly off.
Triple Flash	The indicator shows a sequence of three short flashes (each 200 ms), separated by a short off phase (200 ms). The sequence is finished by a long off phase (1,000 ms).
Double flash	The indicator shows a sequence of two short flashes (each 200 ms), separated by a short off phase (200 ms). The sequence is finished by a long off phase (1,000 ms).
Single flash	The indicator shows one short flash (200 ms) followed by a long "off" phase (1,000 ms).
Flickering (10 Hz)	The indicator turns on and off with a frequency of approximately 10 Hz: on for approximately 50 ms, followed by off for 50 ms. Red and green LEDs shall be on alternately.
Blinking (2,5 Hz)	The indicator turns on and off with a frequency of approximately 2.5 Hz: on for approximately 200 ms, followed by off for 200 ms. Red and green LEDs shall be on alternately.
Flickering (load dependent)	The indicator turns on and off with a frequency of approximately 10 Hz to indicate high Ethernet activity: on for approximately 50 ms, followed by off for 50 ms. The indicator turns on and off in irregular intervals to indicate low Ethernet activity.

Table 65: LED state definitions for the POWERLINK Controlled Node protocol

**Note:** If the node uses a combined LED (S/E), the red and green sub functions are equivalent to the BS and BE LEDs described this chapter. However the BS part is dominant over the BE part, e.g. if BS flash is required, the BE will be turned off during the flash.

## 6.2 Device description file (XDD)

This chapter provides some information and hints about the device description files for Ethernet POWERLINK Controlled Nodes. These files are XML format based and called XDD (XML Device Description) files. Refer to 5 Ethernet Powerlink XML Device Description; EPSG DS 311 V1.2.0; 20 for more details about the XDD files.

The following sections contain steps and examples how to customize the XDD files provided with the Hilscher EPL Controlled Node firmware.

---

**Note:** The information and configuration defined in the XDD file has to be compatible with the stack configuration done by the application.

---

### 6.2.1 Device identification information

The first to customize the XDD file is changing the vendor information. This information has to be edited in the following parts

#### Identity of the EPL Device Profile:

In the EPL Device Profile (ISO15745ProfileContainer->ISO15745Profile-> ProfileHeader-> ProfileIdentification = EPL\_Device\_Profile) edit the element ...->ProfileBody->DeviceIdentity:

```
<DeviceIdentity>
  <vendorName>Hilscher Gesellschaft fuer Systemautomation mbH</vendorName>
  <vendorID>0x44</vendorID>
  <deviceFamily>
    <label lang="en" />
    <description lang="en" />
  </deviceFamily>
  <productName>NETX 51 RE/PLS</productName>
  <productID>0x1E</productID>
  <orderNumber />
</DeviceIdentity>
```

The vendorName and vendorID values are managed by the EPSG (see <http://www.ethernet-powerlink.org>). The elements productName, productID and orderName are defined by the vendor.

#### Identity of Powerlink Communication Profile

In the Powerlink Communication Profile (ISO15745ProfileContainer->ISO15745Profile-> ProfileHeader-> ProfileIdentification = Powerlink\_Communication\_Profile) edit the element ...->ProfileBody-> ApplicationLayers-> identity:

```
<identity>
  <vendorID>0x44</vendorID>
  <productID>0x1E</productID>
</identity>
```

The vendorID value is managed by the EPSG (see <http://www.ethernet-powerlink.org>). The productID is defined by the vendor.

## Identity Object in the OD

In the Powerlink Communication Profile (ISO15745ProfileContainer->ISO15745Profile->ProfileHeader-> ProfileIdentification = Powerlink\_Communication\_Profile) edit the object ...->ProfileBody-> ApplicationLayers-> ObjectList-> Object->index = 1018:

```
<Object index="1018" name="NMT_IdentityObject_REC" objectType="9">
  <SubObject accessType="const" dataType="0005" defaultValue="0x4"
    name="NumberOfEntries" objectType="7" subIndex="00" />
  <SubObject accessType="const" dataType="0007" defaultValue="0x44" name="VendorId_U32"
    objectType="7" subIndex="01" />
  <SubObject accessType="const" dataType="0007" defaultValue="0x1E"
    name="ProductCode_U32" objectType="7" subIndex="02" />
  <SubObject accessType="const" dataType="0007" defaultValue="0x0" name="RevisionNo_U32"
    objectType="7" subIndex="03" />
  <SubObject accessType="const" dataType="0007" defaultValue="0x0" name="SerialNo_U32"
    objectType="7" subIndex="04" />
</Object>
```

The value of VendorId\_U32 is managed by the EPSG (see <http://www.ethernet-powerlink.org>). The values ProductCode\_U32, RevisionNo\_U32 and SerialNo\_U32 are defined by the vendor.



## 6.2.2 Define objects

Since the XDD file has to describe all objects implemented by the node, each new object created by the application has to be also described in the device description file.

For this, just add the new object to the object list (ISO15745ProfileContainer->ISO15745Profile->ProfileHeader->ProfileBody-> ApplicationLayers-> ObjectList in the Powerlink Communication Profile part) with the following structure

### Object from type VAR

```
<Object accessType="" dataType="" defaultValue="" index="" name="" objectType="7" />
```

### Object from type RECORD, ARRAY or DOMAIN

```
<Object index="3000" name="Example_REC" objectType="9">
  <SubObject accessType="const" dataType="0005" defaultValue="0x03"
name="NumberOfEntries" objectType="7" subIndex="00" />
  <SubObject accessType="const" dataType="0005" defaultValue="0x10" name="SubIdx_1"
objectType="7" subIndex="01" />
  <SubObject accessType="ro" dataType="0006" defaultValue="0x0020" name="SubIdx_2"
objectType="7" subIndex="02" />
  <SubObject accessType="rw" dataType="0007" defaultValue="0x00000030" name="SubIdx_3"
objectType="7" subIndex="03" />
</Object>
```

### 6.2.3 Object Dictionary default values

The XDD file should contain a description of all objects implemented by the device within their default value. The object list can be found under ISO15745ProfileContainer->ISO15745Profile->ProfileHeader->ProfileBody-> ApplicationLayers-> ObjectList in the Powerlink Communication Profile part.

To change the default value of an object just search for the corresponding index and subindex and edit the parameter "defaultValue".

Following objects have to be changed depending on the configuration send by the application:

- DeviceType, Index 0x1000. Change this if implementing some standard or vendor specific profiles.
- CycleLen, Index 0x1006. Same value as described in the configuration.
- Identity, Index 0x1018. Changes are described in the chapter above.
- Threshold for CNCollision errors, Index 0x1C0A, Subindex 3. This value has to be changed if the application configures its own value to the stack.
- Threshold for LossSoC errors, Index 0x1C0B, Subindex 3. This value has to be changed if the application configures its own value to the stack.
- Threshold for LossSoA errors, Index 0x1C0C, Subindex 3. This value has to be changed if the application configures its own value to the stack.
- Threshold for LossPReq errors, Index 0x1C0D, Subindex 3. This value has to be changed if the application configures its own value to the stack.
- Threshold for CNCrc errors, Index 0x1C0F, Subindex 3. This value has to be changed if the application configures its own value to the stack.
- IP Settings, Index 0x1E40, Subindex 2. This value has to be changed to 192.168.100.NodeId
- Default Gateway, Index 0x1E40, Subindex 5. Same value as described in the configuration.
- FeatureFlags, Index 0x1F82. Same value as described in the configuration.
- NodeId, Index 0x1F93, Subindex 1. Same value as described in the configuration.

## 6.2.4 PDO configuration

This section provides the steps to be followed to changed successfully the PDO configuration in the XDD file and make it compatible with the configuration defined by the application.

---

**Note:** For devices with static PDO mapping and default objects created by the stack, the PDO layout and mapping has to follow the rules described in section *PDO objects configuration* on page 61.

---

### Create user data objects

First of all, create the PDO objects (range 0x2000 – 0x5FFF) with the desired PDO layout. Refer to section *Define objects* on page 113 for more information about defining new objects in the XDD file.

### Create the PDO mapping configuration

Adjust the mapping configuration to the new PDO layout by editing the objects 0x1600 and 0x1A00 beginning with index 1. The coding of the mapping entries is described in details in section *Configuring a receive PDO* (page 31) and *Configuring transmit PDO* (page 33).

### Define the PDO mapping version (Optional)

Change the mapping version in the objects 0x1400 and 0x1800 subindex 2.

### Change PDO size

Adjust the PDO size and max size in the object 0x1F98, Subindices 1, 2, 4 and 5.

## 6.3 Legal notes

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## 6.5 List of figures

Figure 1: Structure of Ethernet POWERLINK Controlled Node stack .....	14
Figure 2: State diagram of the Ethernet POWERLINK Controlled Node - Initialization .....	17
Figure 3: State diagram of the Ethernet POWERLINK Controlled Node - Communicating .....	19
Figure 4: Configuration sequence with Auto-start mode .....	57
Figure 5: Configuration Sequence with Start-by-Application Mode .....	58
Figure 6: Sequence of static mapping configuration with default PDO objects .....	60
Figure 7: Sequence of static mapping configuration with user defined PDO objects .....	63
Figure 8: Sequence of dynamic mapping configuration .....	65
Figure 9: Sequence of Set PDO Size service.....	69
Figure 10: Example of successful NMT state change .....	76
Figure 11: Example of erroneous NMT state change .....	77



## 6.6 List of tables

Table 1: List of revisions .....	4
Table 2: Technical data .....	6
Table 3: Availability of firmware/stack .....	6
Table 4: Terms, abbreviations and definitions .....	8
Table 5: References to documents .....	9
Table 6: Overview .....	10
Table 7: General structure of Object Dictionary .....	22
Table 8: Definition of Objects .....	23
Table 9: Available Data Type Definitions – Part 2 .....	27
Table 10: Communication Profile - General Overview .....	30
Table 11: Receive PDO mapping - Object mapping - Interpretation of values .....	32
Table 12: Transmit PDO mapping - Object mapping - Interpretation of values .....	33
Table 13: Meaning of bCurrentState and bTargetState .....	37
Table 14: Possible values for parameter usPdInHskTriggerType .....	39
Table 15: Possible values for parameter usPdOutHskTriggerType .....	39
Table 16: Possible values for parameter usSyncHskTriggerType .....	39
Table 17: Value for Stack Configuration Flags .....	45
Table 18: Value for use of Custom Threshold flags .....	46
Table 19: Meaning of the Feature Flags .....	48
Table 20: EPLCN_IF_SET_CONFIG_REQ – Configure Stack Request .....	54
Table 21: EPLCN_IF_SET_CONFIG_CNF – Configure Stack Confirmation .....	55
Table 22: Stack Configuration Flags for static mapping configuration with default PDO objects .....	59
Table 23: Feature Flags for static mapping configuration with default PDO objects .....	59
Table 24: Example of PDO default objects configuration .....	61
Table 25: Stack Configuration Flags for static mapping configuration with user defined PDO objects .....	62
Table 26: Feature Flags for static mapping configuration with user defined PDO objects .....	62
Table 27: Stack Configuration Flags for dynamic mapping configuration .....	64
Table 28: Additional Stack Configuration Flag for dynamic mapping configuration with cross traffic .....	64
Table 29: Feature Flags for dynamic mapping configuration .....	64
Table 30: EPLCN_IF_SET_PDO_SIZE_REQ – Set PDO Size request .....	67
Table 31: EPLCN_IF_SET_PDO_SIZE_CNF – Set PDO Size confirmation .....	68
Table 32: EPLCN_IF_SET_NODE_ID_REQ – Set NodeId request .....	71
Table 33: EPLCN_IF_SET_NODE_ID_CNF – Set NodeId confirmation .....	72
Table 34: NMT state transitions controlled by the application .....	73
Table 35: EPLCN_IF_NMT_SET_STATE_REQ – Set NMT State request .....	74
Table 36: EPLCN_IF_NMT_SET_STATE_CNF – Set NMT State confirmation .....	75
Table 37: RCX_REGISTER_APP_REQ – Register for Status Indications request .....	78
Table 38: RCX_REGISTER_APP_CNF – Register for Status Indications confirmation .....	79
Table 39: RCX_UNREGISTER_APP_REQ – Unregister for Status Indications request .....	80
Table 40: RCX_UNREGISTER_APP_CNF – Unregister for Status Indications confirmation .....	81
Table 41: EPLCN_IF_NMT_STATE_CHANGE_IND – Current NMT State indication .....	82
Table 42: EPLCN_IF_NMT_STATE_CHANGE_RES – Current NMT State response .....	83
Table 43: EPLCN_IF_NMT_CMD_ENABLE_RDY_TO_OPERATE_IND – NMT Command EnableReadyToOperate indication .....	84
Table 44: EPLCN_IF_NMT_CMD_ENABLE_RDY_TO_OPERATE_RES – NMT Command EnableReadyToOperate response .....	85
Table 45: EPLCN_IF_CROSS_TRAFFIC_CONFIGURED_IND – Cross traffic configured indication .....	87
Table 46: EPLCN_IF_CROSS_TRAFFIC_CONFIGURED_RES – Cross traffic configured response .....	88
Table 47: Static Error Bit Field .....	90
Table 48: Structure of an Error / Status Entry .....	91
Table 49: Entry type .....	92
Table 50: Error Entries within Status Response Frames - Hardware errors .....	92
Table 51: Error Entries within Status Response Frames - Frame size errors .....	92
Table 52: Error Entries within Status Response Frames – Loss of frame errors .....	92
Table 53: EPLCN_IF_NMT_WRITE_STATIC_ERROR_BIT_FIELD_REQ – Write Static Error Bit Field request .....	94
Table 54: EPLCN_IF_NMT_WRITE_STATIC_ERROR_BIT_FIELD_CNF – Write Static Error Bit Field confirmation .....	95
Table 55: EPLCN_IF_NMT_WRITE_ERROR_ENTRY_REQ – Write Error Entry request .....	97
Table 56: EPLCN_IF_NMT_WRITE_ERROR_ENTRY_CNF – Write Error Entry confirmation .....	98
Table 57: EPLCN_IF_NMT_WRITE_STATUS_ENTRY_REQ – Write Status Entry request .....	100
Table 58: EPLCN_IF_NMT_WRITE_STATUS_ENTRY_CNF – Write Status Entry confirmation .....	101
Table 59: EPLCN_IF_NMT_NEW_ERROR_ENTRY_IND – New Error Entry indication .....	103
Table 60: EPLCN_IF_NMT_NEW_ERROR_ENTRY_RES – New Error Entry response .....	104
Table 61: EPLCN_IF_NMT_NEW_STATUS_ENTRY_IND – New Status Entry indication .....	106
Table 62: EPLCN_IF_NMT_NEW_STATUS_ENTRY_RES – New Status Entry response .....	107

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Table 63: Status codes / Error codes .....	108
Table 64: LED states for the POWERLINK Controlled Node .....	109
Table 65: LED state definitions for the POWERLINK Controlled Node protocol .....	110

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